



Moving towards the future of
power engineering

Production of High Temperature Superconducting Tapes Enhanced by Computational Thermochemistry

GTT-Technologies Workshop, June 29th-July 1st , 2016

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Heisenbergstr. 16
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outlook

- Deutsche Nanoschicht
- Superconductivity
- 2G HTS tapes
- Expanded Pilot Production Line
- Project ELSA
- summary

Deutsche Nanoschicht GmbH

- Founded November 2011 by Dr. Michael Bäcker
- Management-buy-out of former HTS wire group of insolvent Zenergy Power GmbH
- Located in Rheinbach, Germany
- Operational since January 2012
- 50 employees (chemists, physicists, engineers, technicians, worker)
- Expertise in HTS wires, chemical solution deposition, ceramic functional layers, ink-jet-printing, epitaxial growth



Superconductors – Huge Current Carrying Capability

Breaking paradims in electro technology

Superconductors conduct electricity with no resistance – enabling 2 key properties:



- 100% Efficiency : No losses – resulting in a dramatic reduction on CO₂
- 100x Capacity : Dramatic reduction in material use

3 Energy Sectors



Renewable Energy

Power Generation

Hydro, Wind



Smart Grid

Transmission & Distribution

Fault Current Limiters



Industrial Machines

Power Conversion

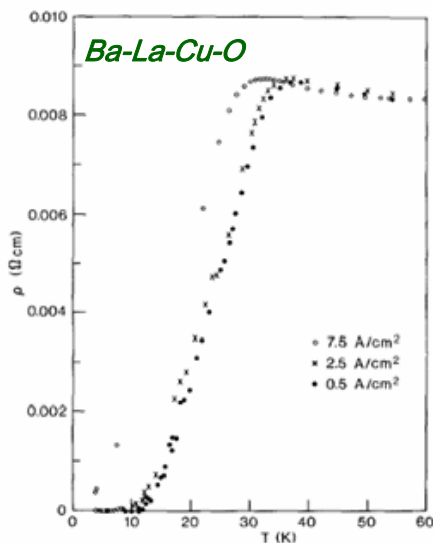
Induction Heater



outlook

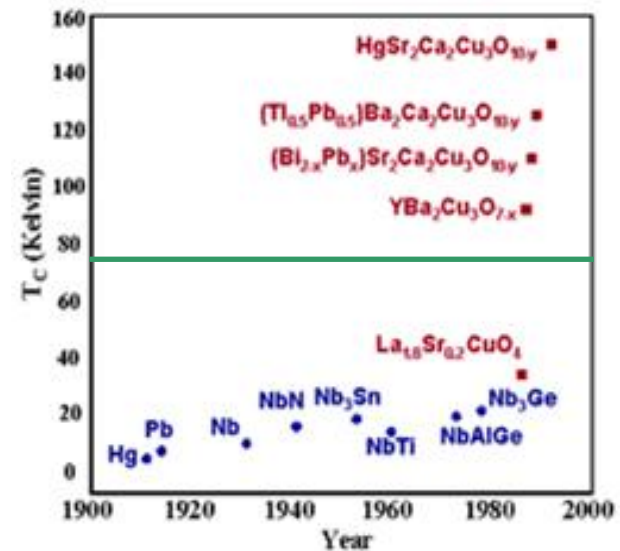
- Deutsche Nanoschicht
- Superconductivity
 - High Temperature Superconductors (HTS)
 - Anisotropy and critical parameters
- 2G HTS tapes
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High Temperature Superconductors



Ba-La-Cu-O
compound: J.
G. Bednorz, K. A.
Müller, *Z. Physik, B*
64 (1986) 189

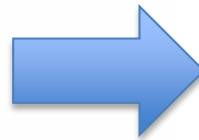
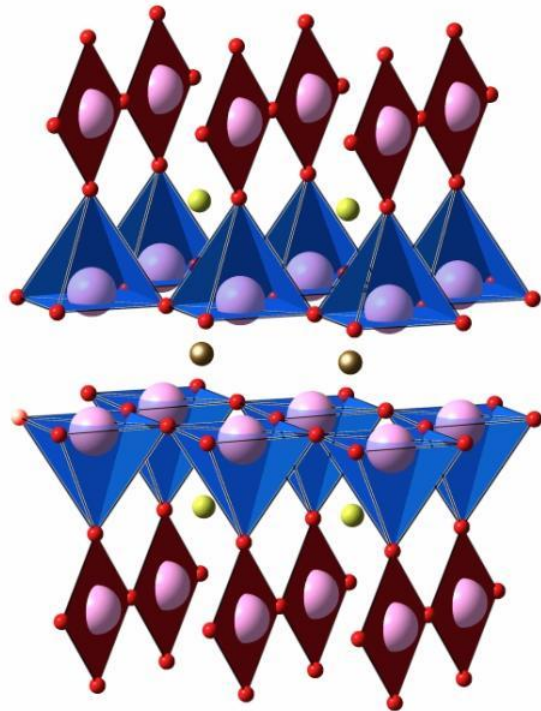
commercial
potential



compound		T_c (K)
YBa ₂ Cu ₃ O _{7-δ}	Y-123	92
Bi ₂ Sr ₂ CaCu ₂ O ₈	Bi-2212	84
Bi ₂ Sr ₂ Ca ₂ Cu ₃ O ₁₀	Bi-2223	110
TlBa ₂ Ca ₂ Cu ₃ O ₁₀	Tl-1223	125
HgBa ₂ Ca ₂ Cu ₃ O ₁₀	Hg-1223	133

High Temperature Superconductors

- Challenges in materials



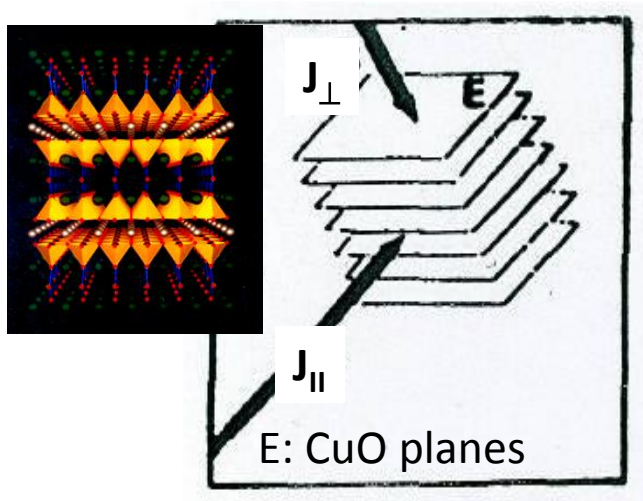
- Complex ceramic oxides: $\text{YBa}_2\text{Cu}_3\text{O}_x$
- Complex layered crystalline structure
- Strong anisotropic behaviour



Superconducting current:
parallel CuO-planes \gg perpendicular CuO-planes

Layered microstructure similar to "graphite or mica"

Anisotropy in HTS materials



J_{\parallel} : current parallel to CuO plane

J_{\perp} : current perpendicular to CuO plane

$$J_{\parallel} \gg J_{\perp}$$

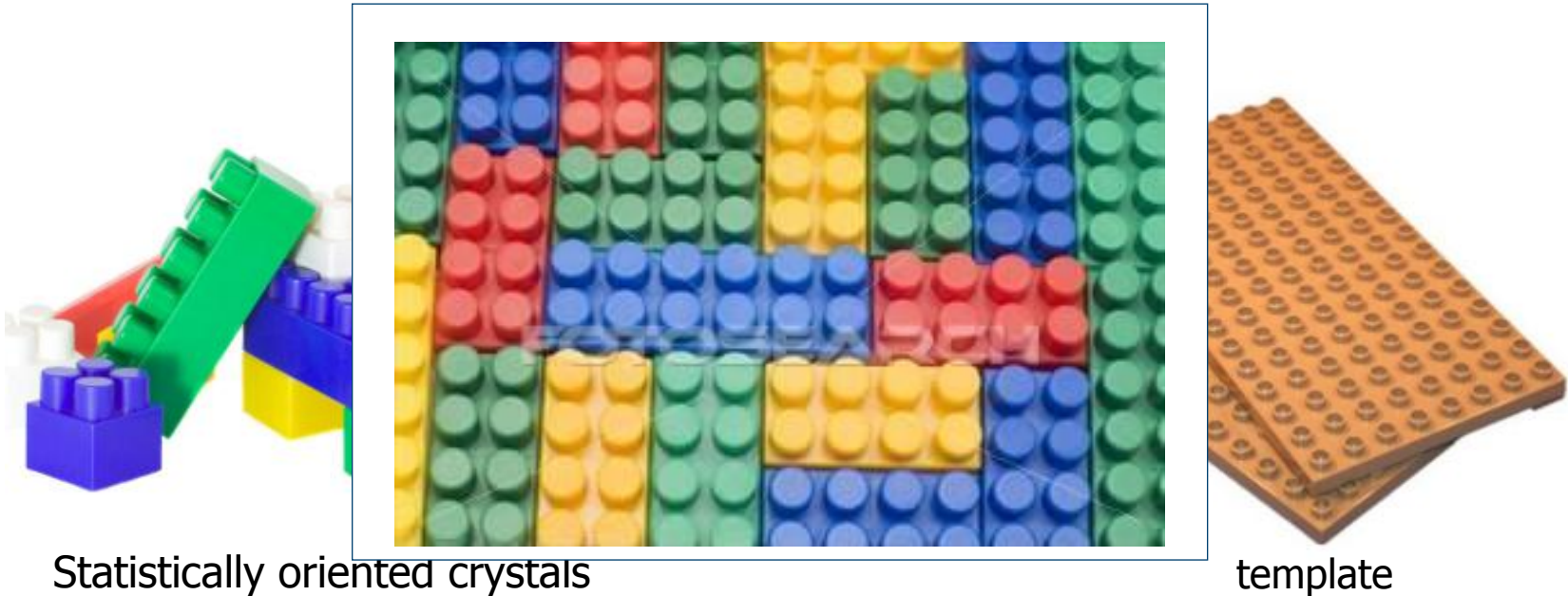
\Rightarrow all CuO planes have to be parallel orientated

Polycrystalline, non orientated



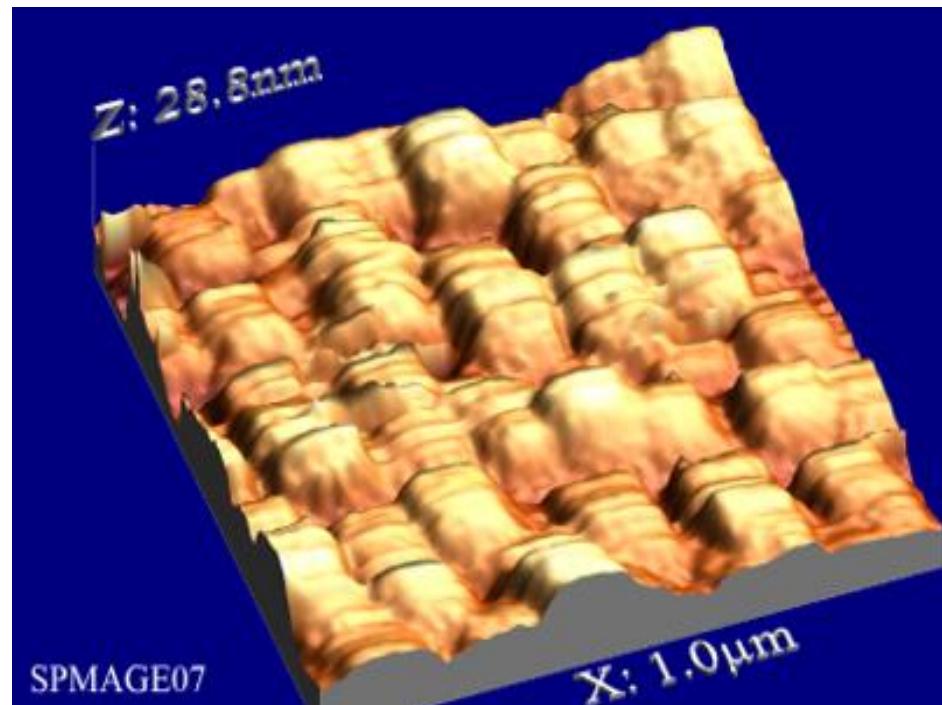
Second Generation (2G) HTS

- HTS Architecture – textured epitactic crystal growth



Zweite Generation (2G) HTS Leiter

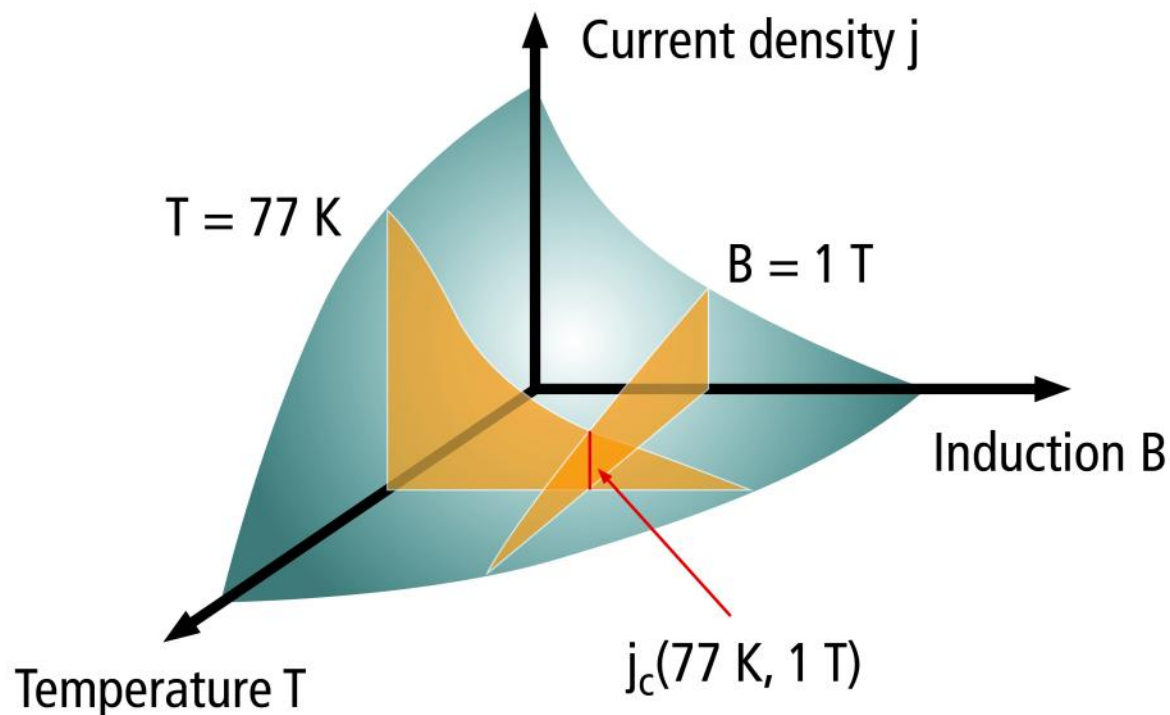
- HTS Architecture – textured epitactic crystal growth



AFM–analysrs of a Ceriumoxide buffer layer

High Temperature Superconductors

- Critical parameters

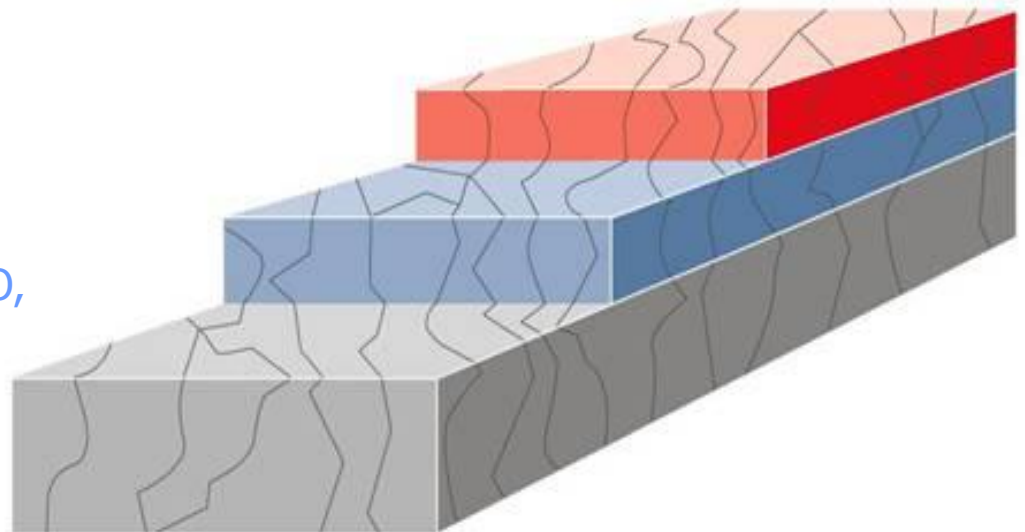


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- Superconductivity
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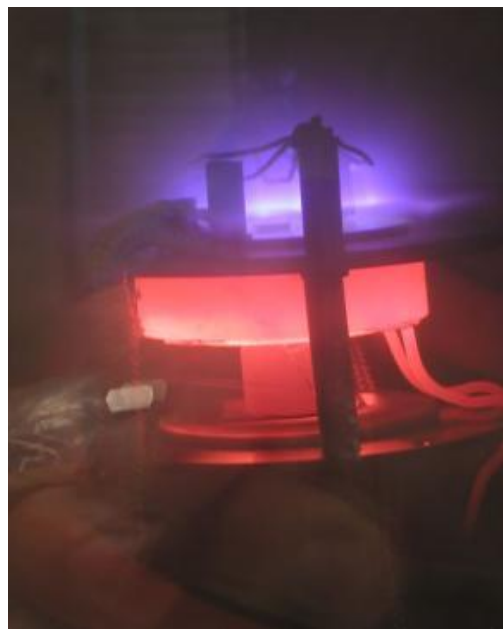
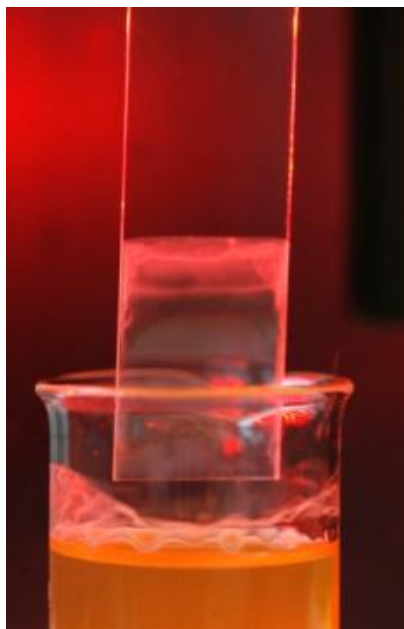
Layer architecture of (2G) HTS tape

- Protective metal layer:
silver, gold, copper
- Superconductor layer:
 $\text{YBa}_2\text{Cu}_3\text{O}_x$, $\text{REBa}_2\text{Cu}_3\text{O}_x$
- Buffer layer:
 MgO , ZrO , GZO , YSZ , Y_2O_3 , CeO_2 , LZO ,
 STO
- Metal alloy substrate:
Hastelloy, NiCroFer, Ni-alloy

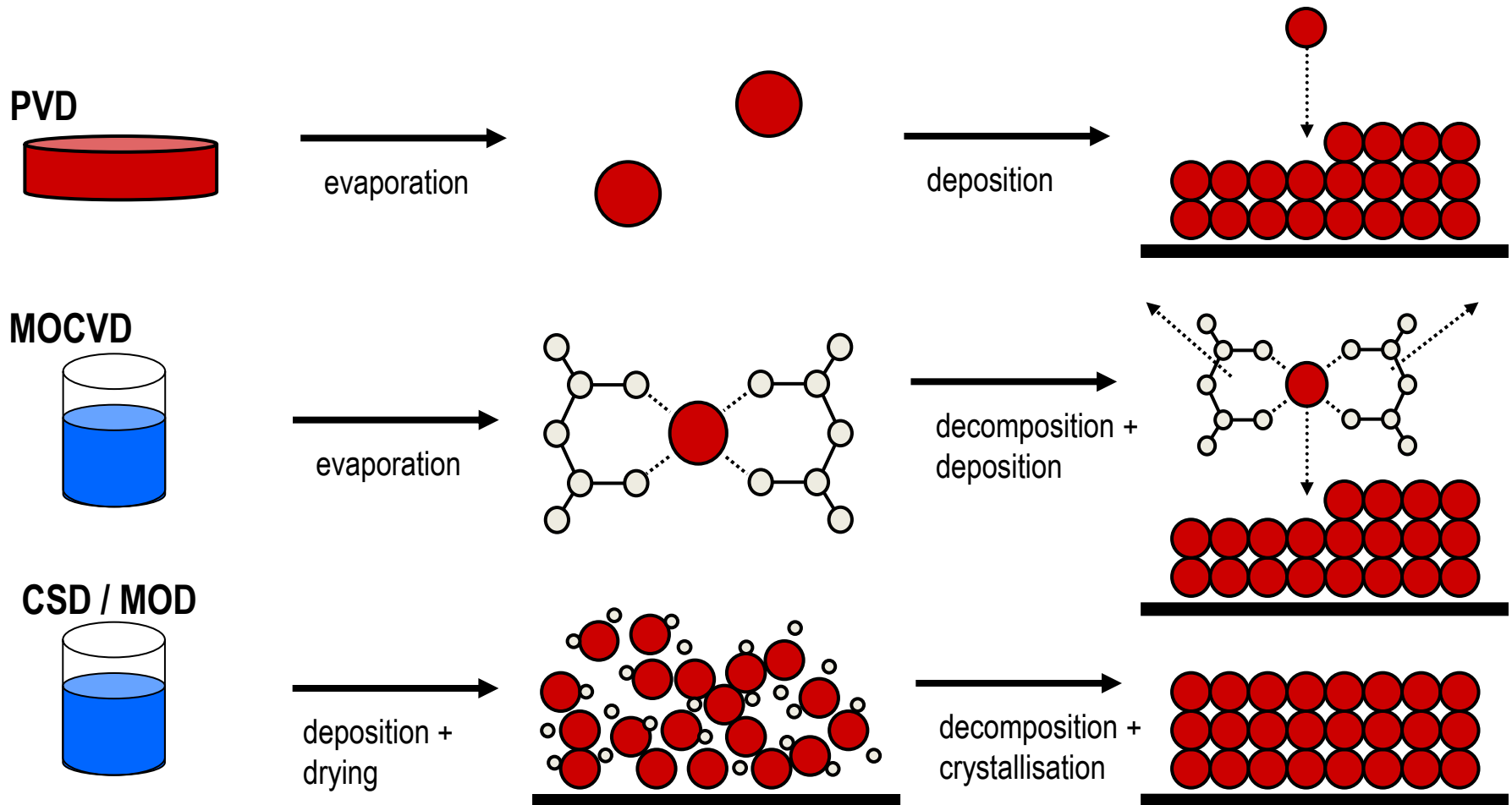


Deposition Processes

- Chemical solution deposition (CSD)
- Chemical vapour deposition (CVD)
- Physical vapour deposition (PVD)

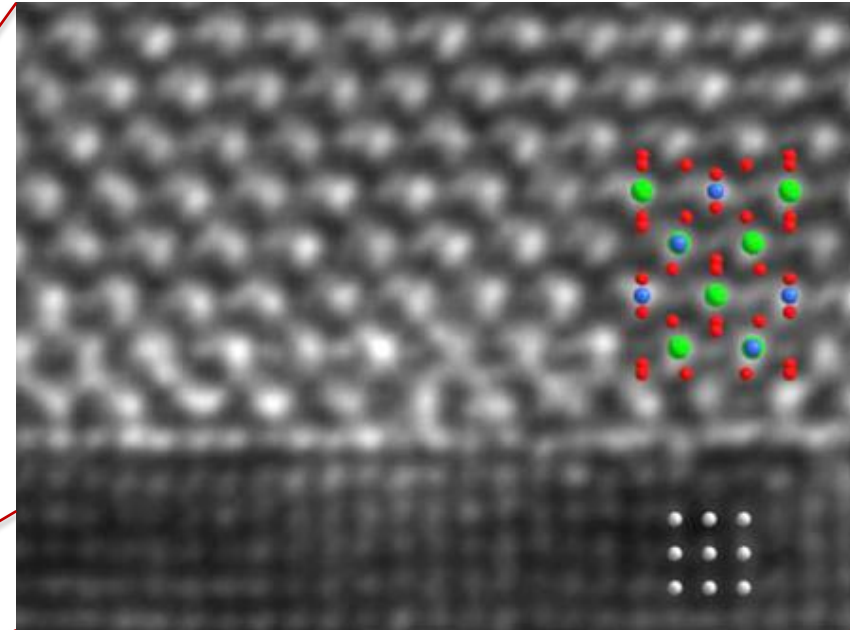
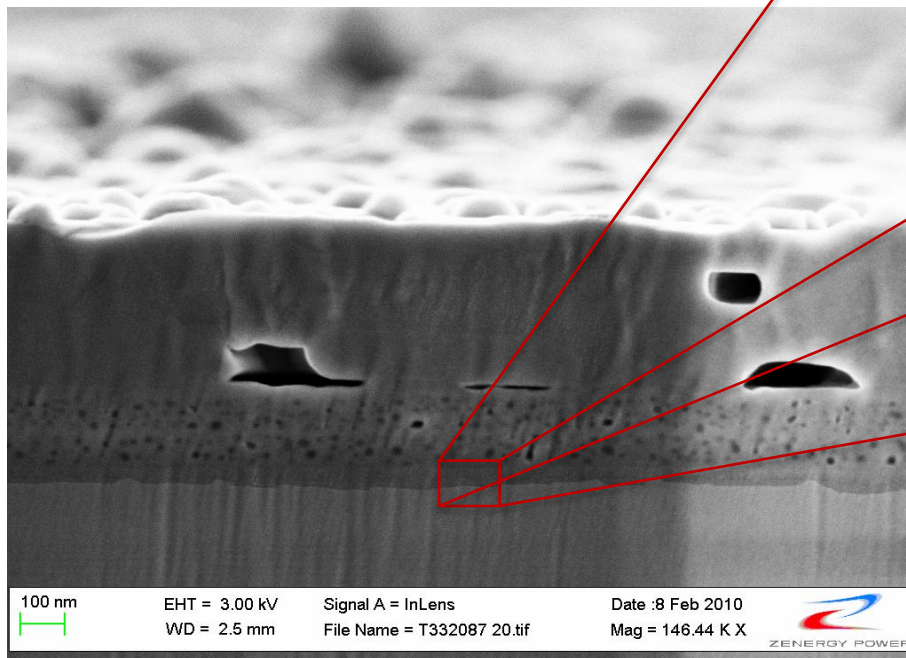


Layer architecture of (2G) HTS tape



Process Technology

- Ceramic multi-layer architecture on metallic substrate



Epitaxial (orientated) growth of ceramic functional layers on textured metallic substrate

"all-solution" Coated Conductor

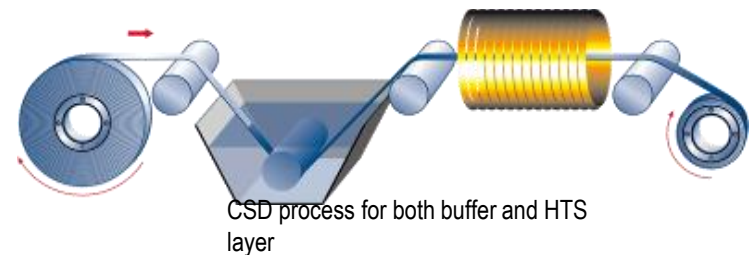
- Chemical solution deposition is considered to be the "most promising and most challenging process"

- **Advantages:**

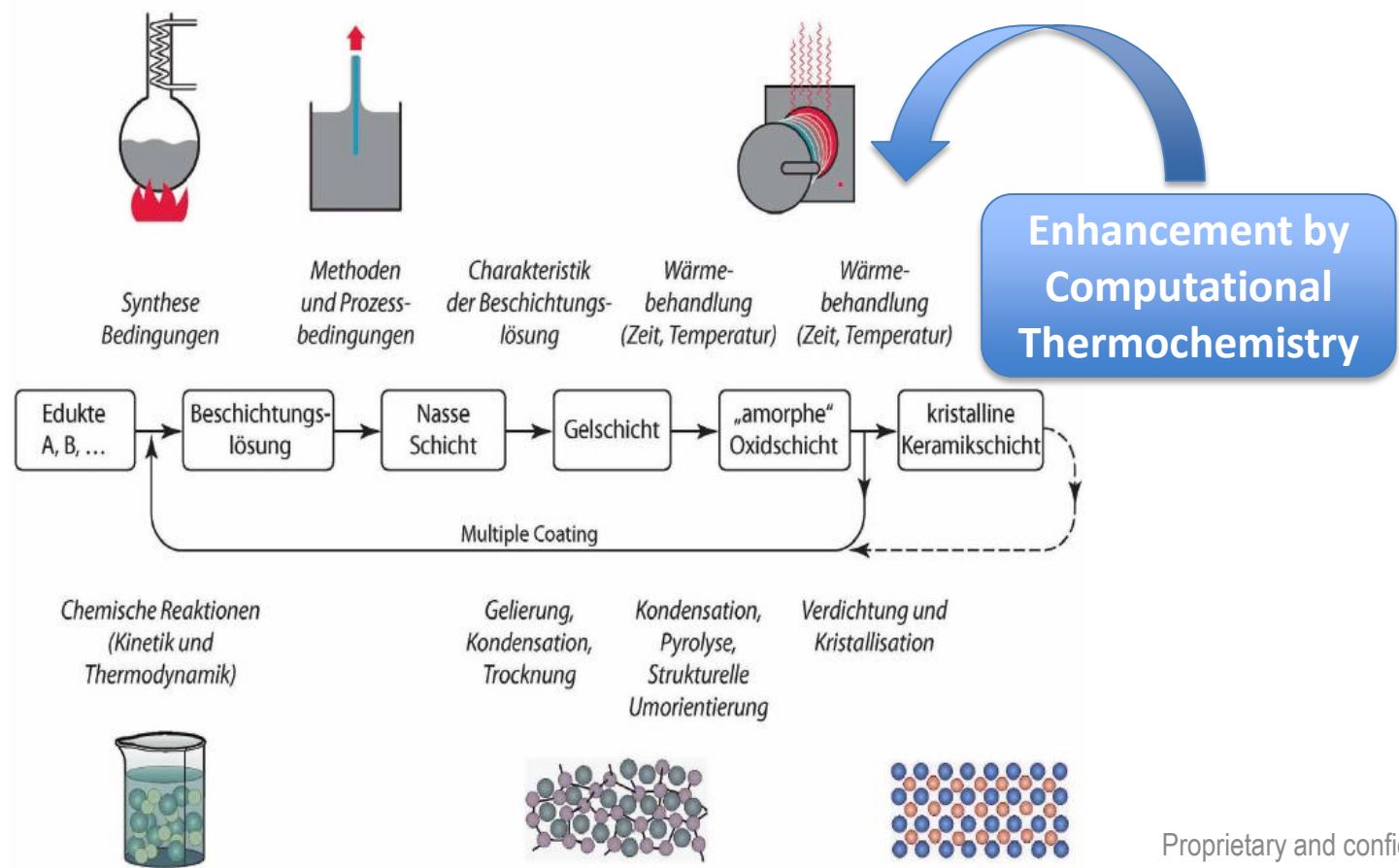
- highest throughput (deposition rates)
- lowest investment
- lowest energy consumption
- low raw material costs

- Main disadvantage:

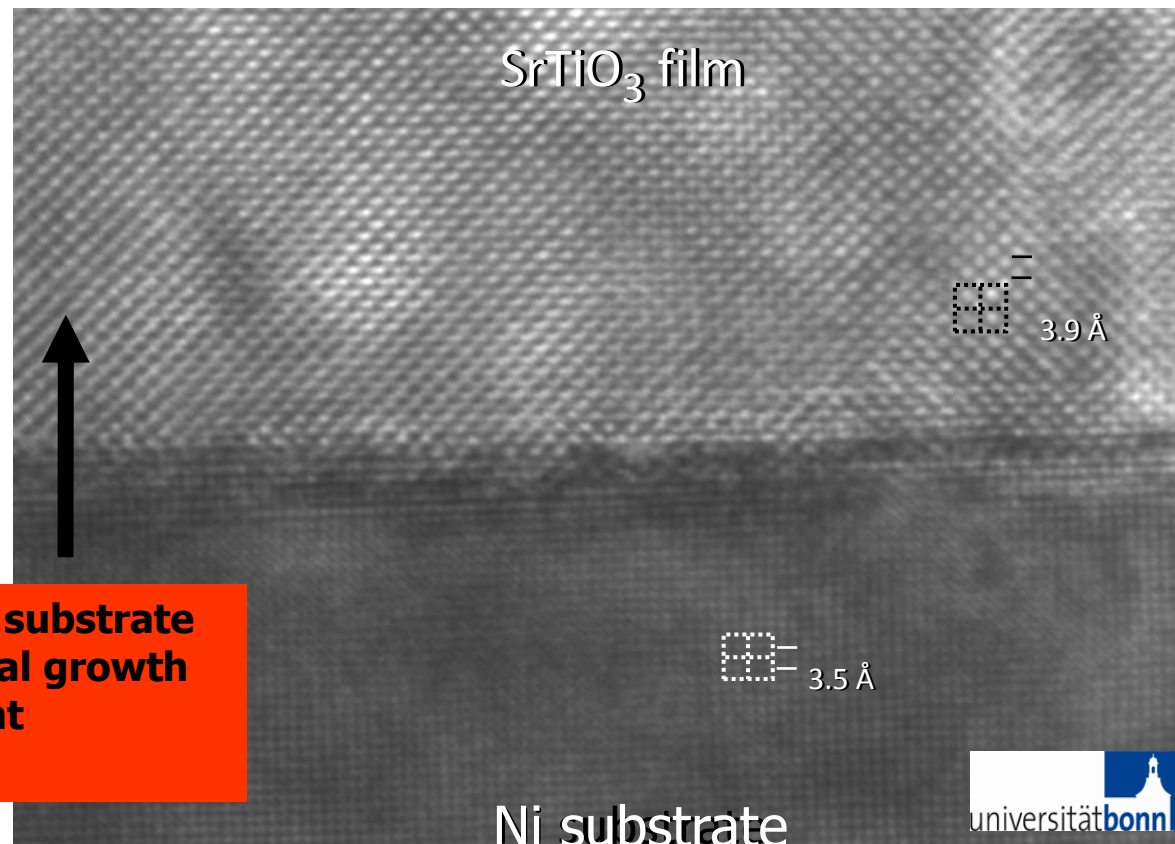
CSD processes only yield polycrystalline / untextured films



"all-solution" Coated Conductor

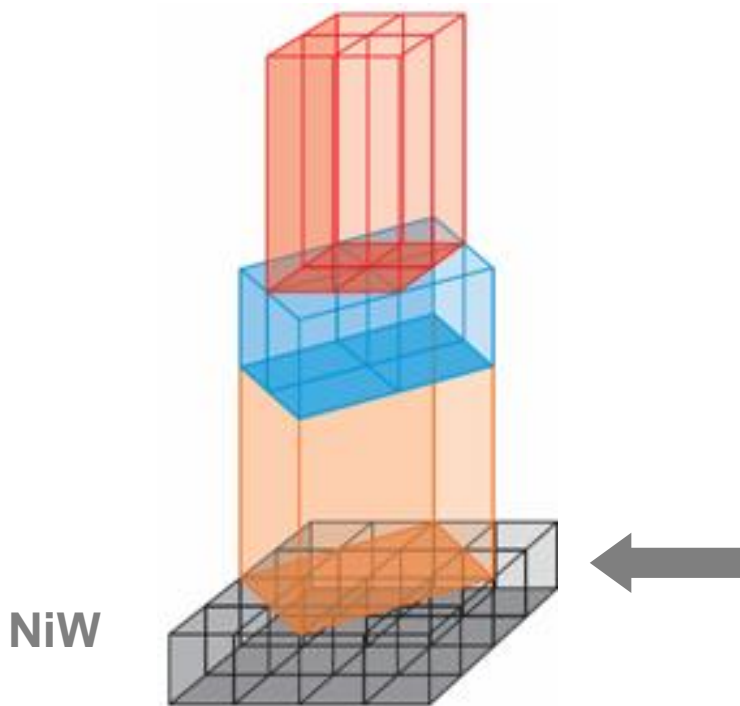


"all-solution" Coated Conductor



"all-solution" Coated Conductor

- Standard architecture for all-solution Coated Conductor at Deutsche Nanoschicht

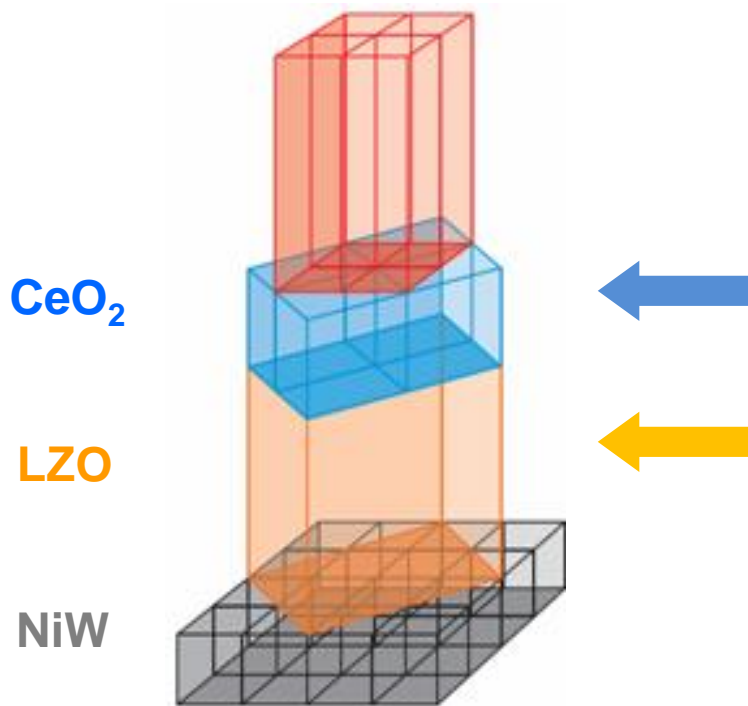


CSD processes normally result in untextured growth

⇒ biaxially textured substrate required

"all-solution" Coated Conductor

- Standard architecture for all-solution Coated Conductor at Deutsche Nanoschicht



Buffer layers for

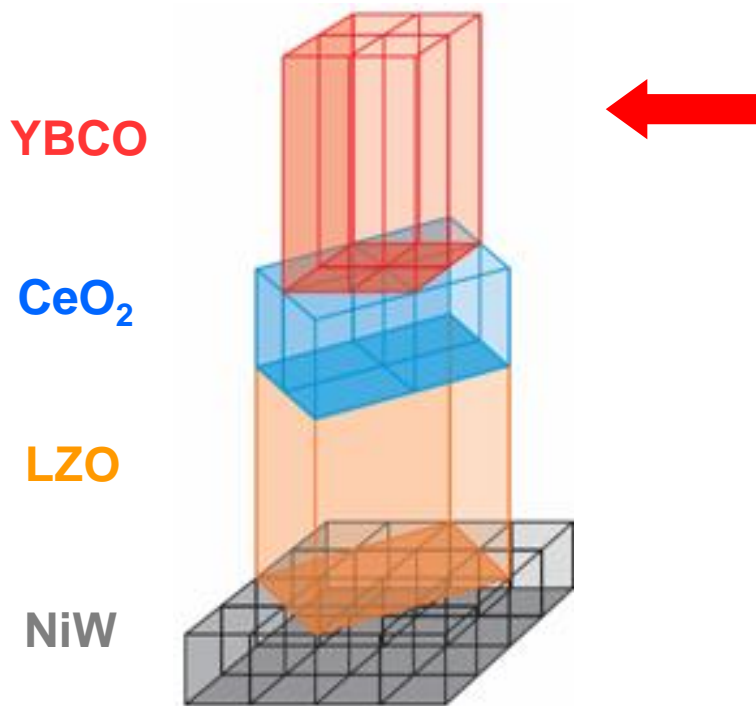
- texture transfer
- Nickel & Oxygen diffusion barrier

Special requirements for CSD processes:

- chemically inert
- low lattice mismatch

"all-solution" Coated Conductor

- Standard architecture for all-solution Coated Conductor at Deutsche Nanoschicht

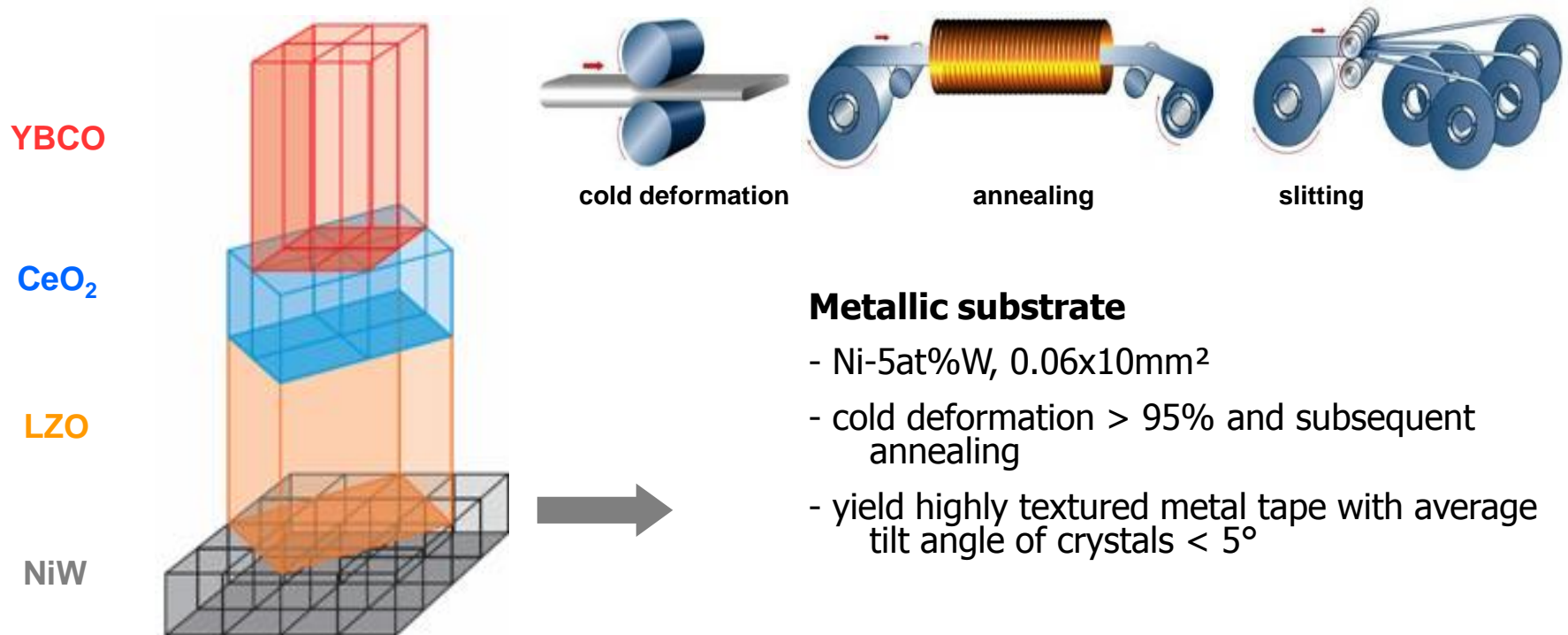


HTS layer:

- TFA YBCO route
- single or multi-layer deposition

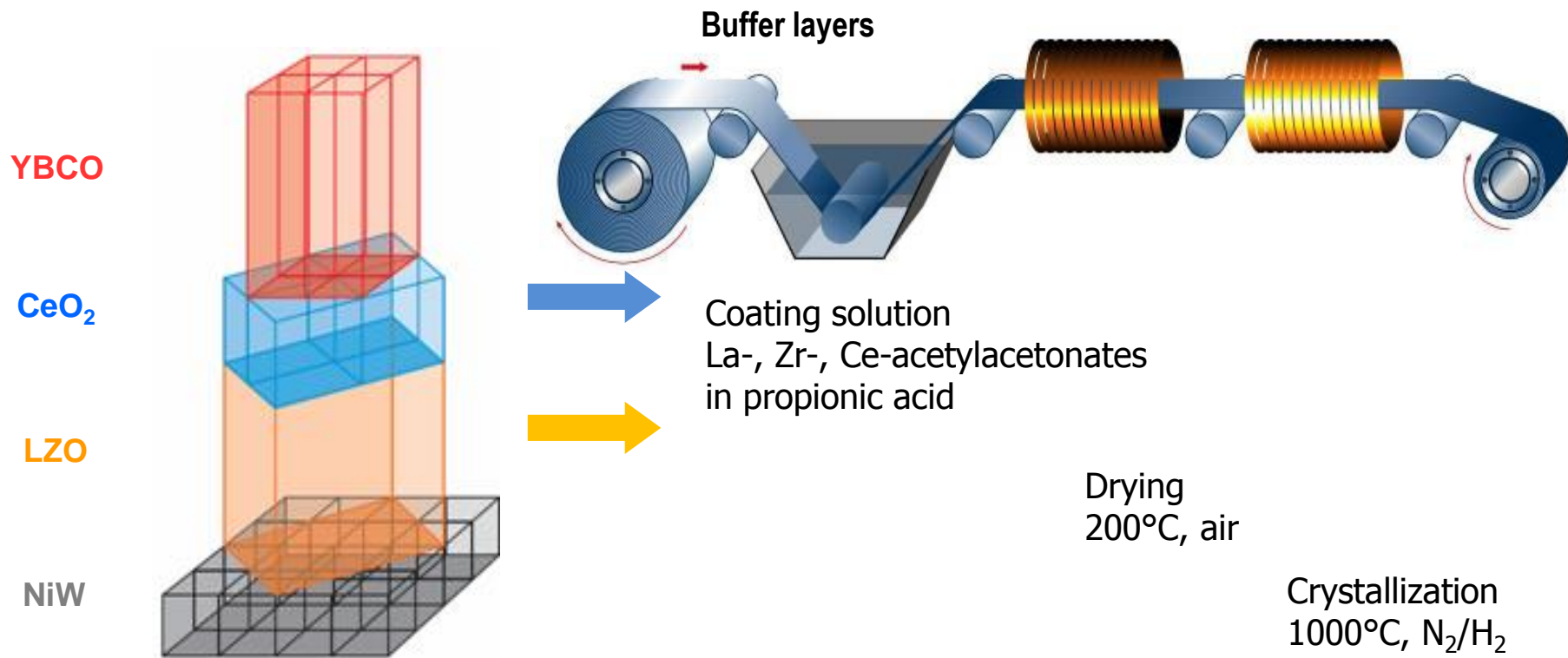
Basic processes

- Standard architecture for all-solution Coated Conductor



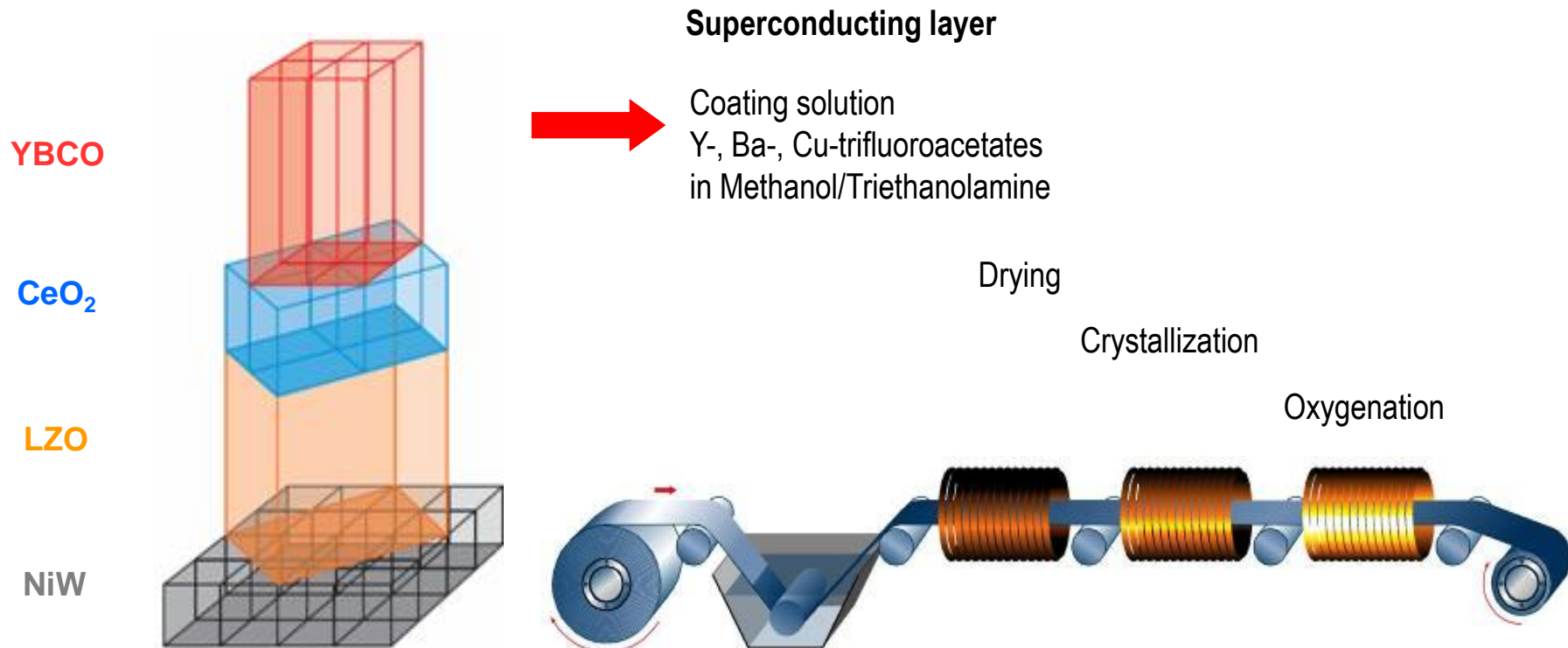
Basic processes

- Standard architecture for all-solution Coated Conductor



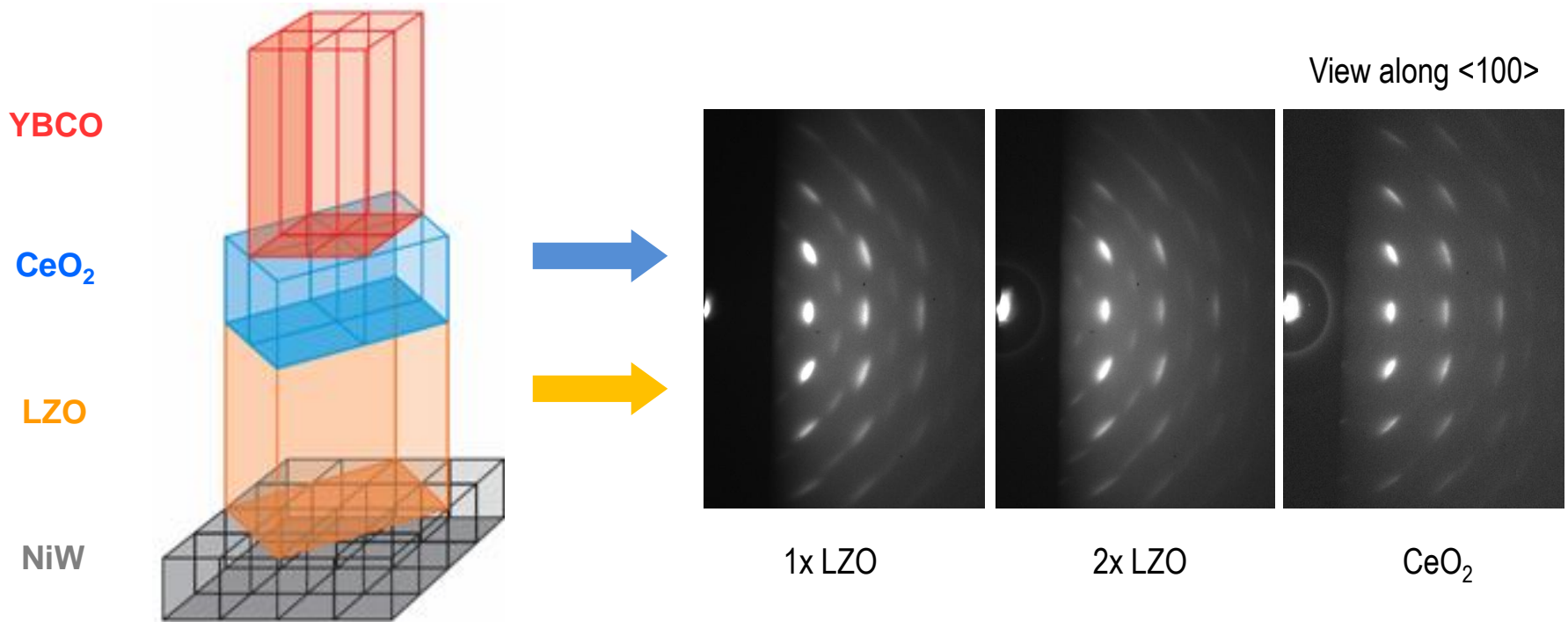
Basic processes

- Standard architecture for all-solution Coated Conductor



Texture analysis: RHEED

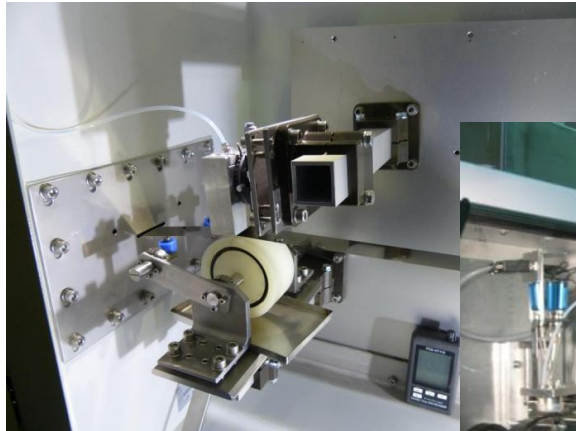
- Standard architecture for all-solution Coated Conductor



Length & Performance

Continuous processing: reel-to-reel devices

- >15 continuous processes / reel-to-reel devices
- RTR concept proven for all processes



Slot die of coating solutions



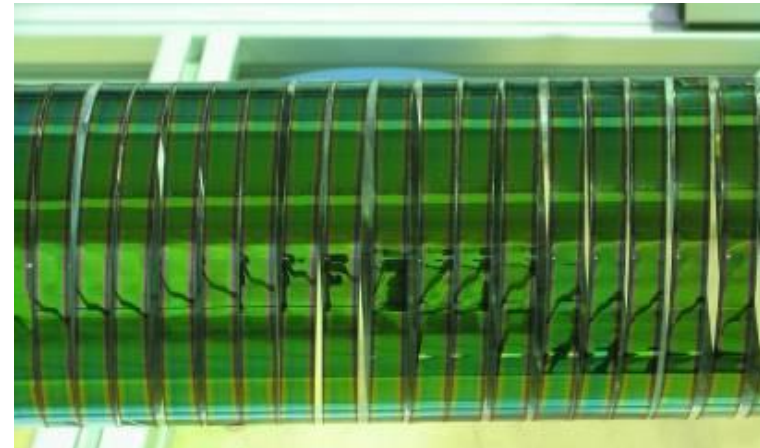
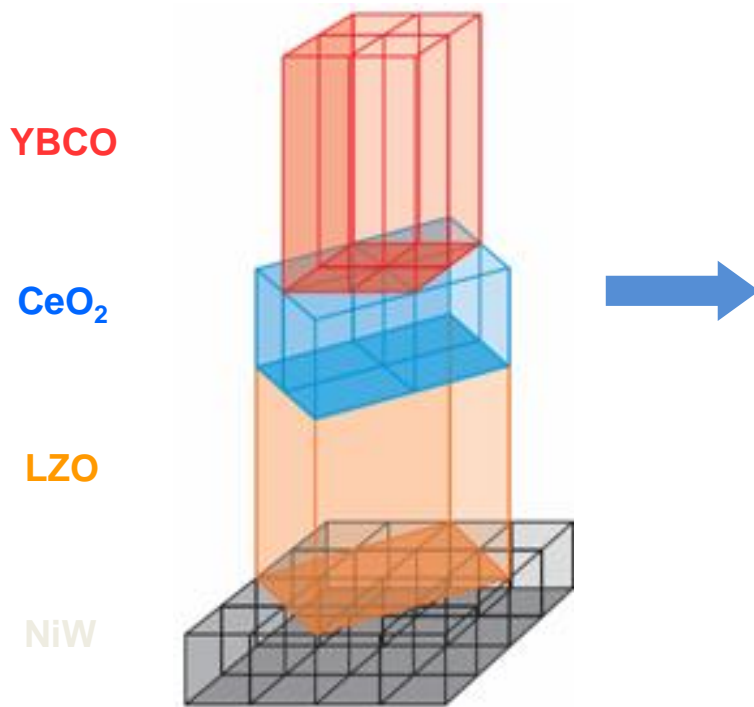
Ink-jet printing of coating solutions



All devices in-house designed and assembled

Length & Performance

- Processing of all-solution Coated Conductor



Continuously processed
multi-buffer-layer

Expanded pilot line

- EPL construction until end 2015
- Planned capacity > 200km technical HTS wire
- Start sampling for projects mid 2016



Lab processing



Expanded Pilot Line

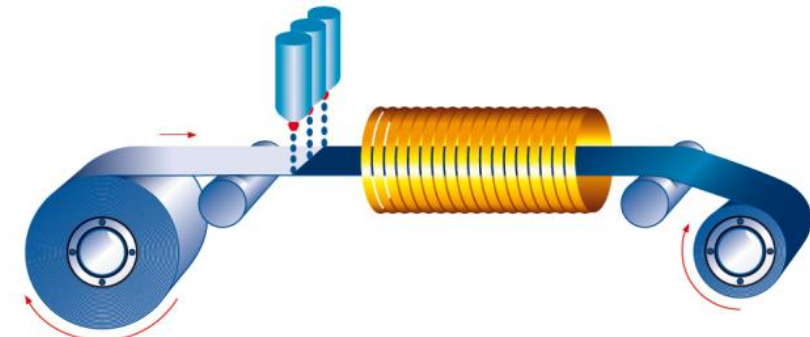
Process Technology

- Chemical solution deposition

Advantages:

- highest throughput (deposition rates)
- lowest investment
- lowest energy consumption
- low raw material costs

⇒ **favourable for energy applications**

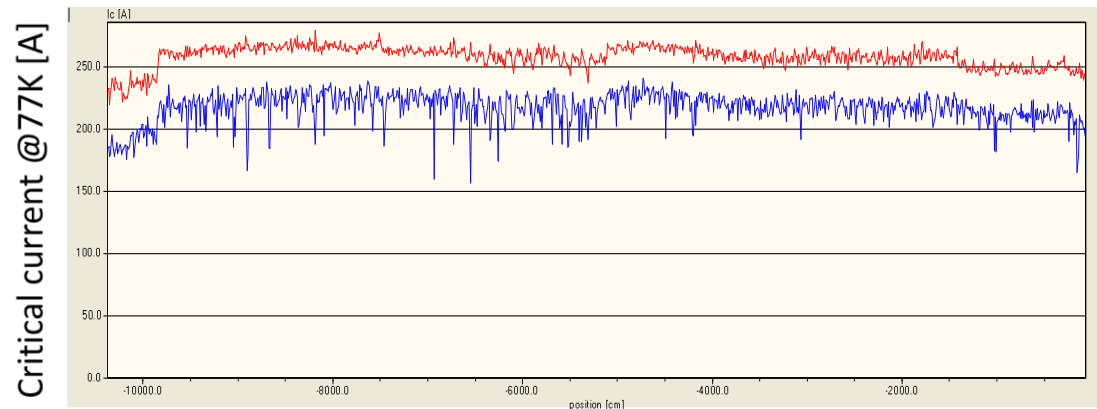
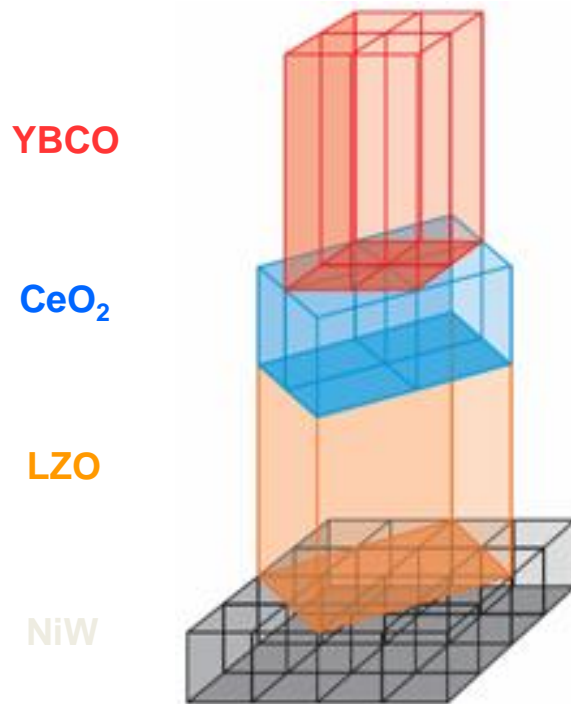


Continuous coating and annealing



Length & Performance

- Superconducting properties of all-solution Coated Conductor

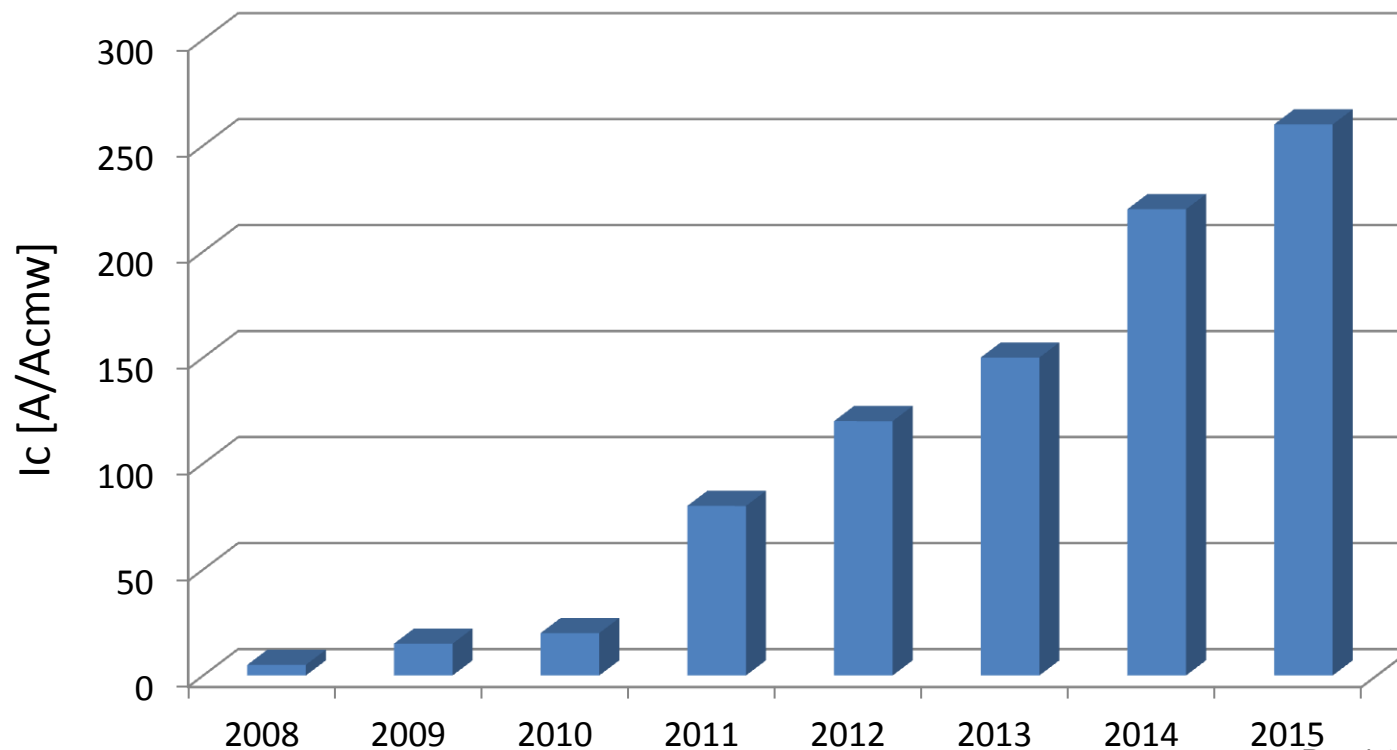


100 m tape length, 10 mm wide
Current density 1.7 MA/cm²

Performance

VDM Metals
Honeywell
Heraeus

- Development with industrial partners over nearly 10 years
 - Sample lengths increased from 5m (2018) towards >50m (2015)



Project ELSA (2006-2010)

- ELSA - "Entwicklung von SupraLeitern mit SchichtArchitektur"
 - "Development of Superconductors with Layer-Achitecture"

- Consortium



Federal Ministry
of Economics
and Technology



This work was funded by the
German Ministry of Economy
(BMWi) under project number
0327433

GTT - TECHNOLOGIES

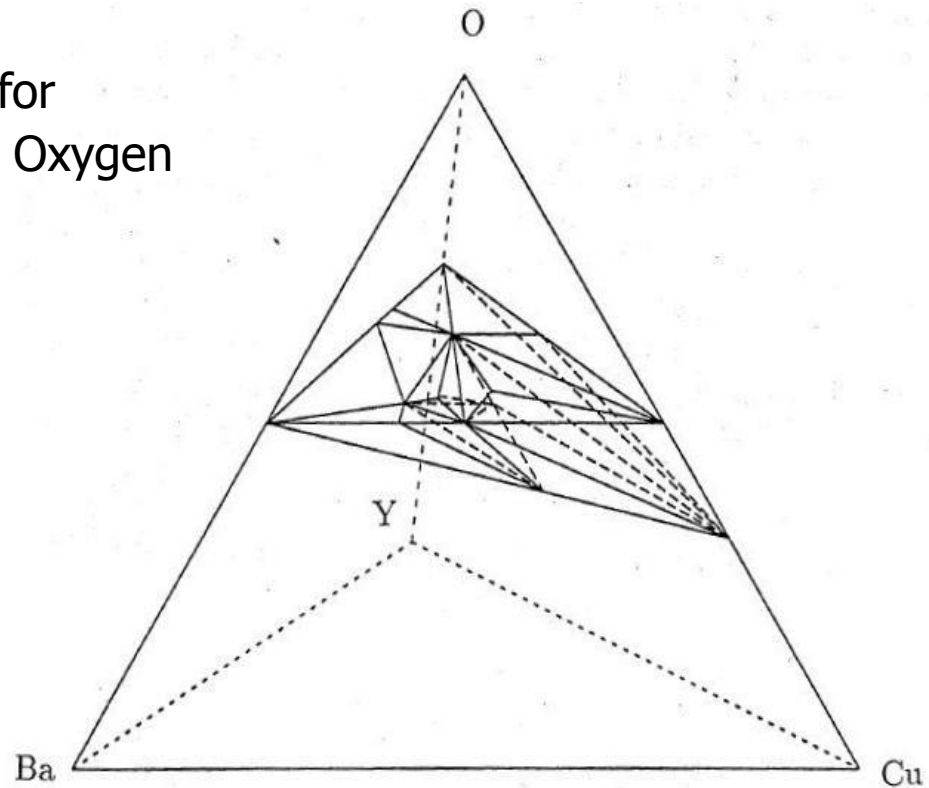


EBERHARD KARLS
UNIVERSITÄT
TÜBINGEN



Project ELSA

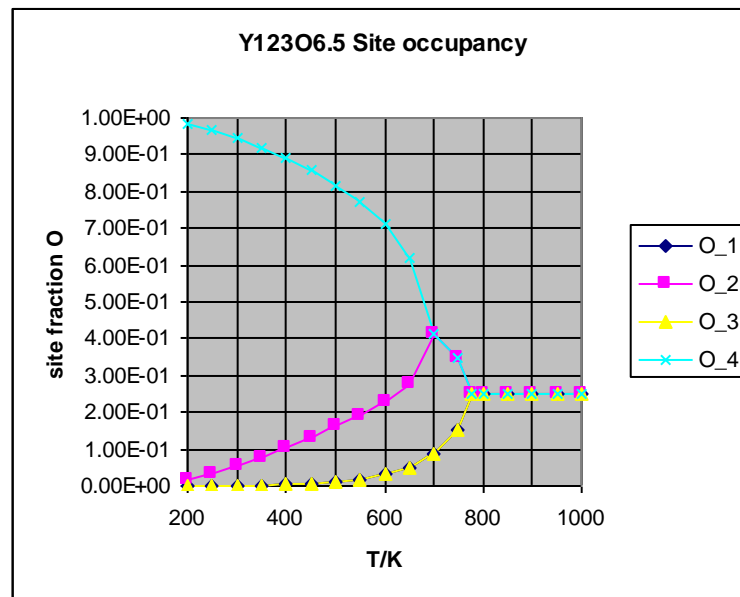
- Highlights
 - Quaternary phase diagram for Yttrium - Barium - Copper - Oxygen





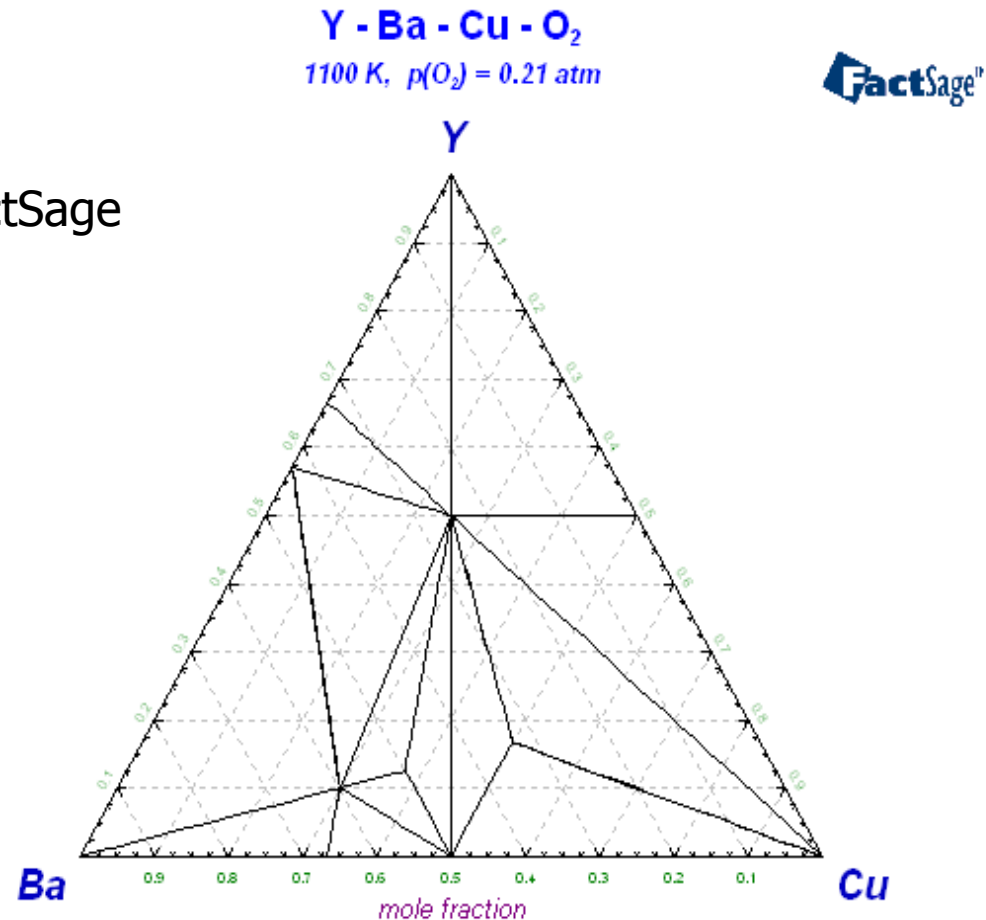
Project ELSA

- Highlights
 - Oxygen on sub-lattices in $\text{YBa}_2\text{Cu}_3\text{O}_{(6+x)}$
 - Tetragonal
 - Orthorhombic'
 - Orthorhombic''



Project ELSA

- Highlights
 - Full calculation in FactSage



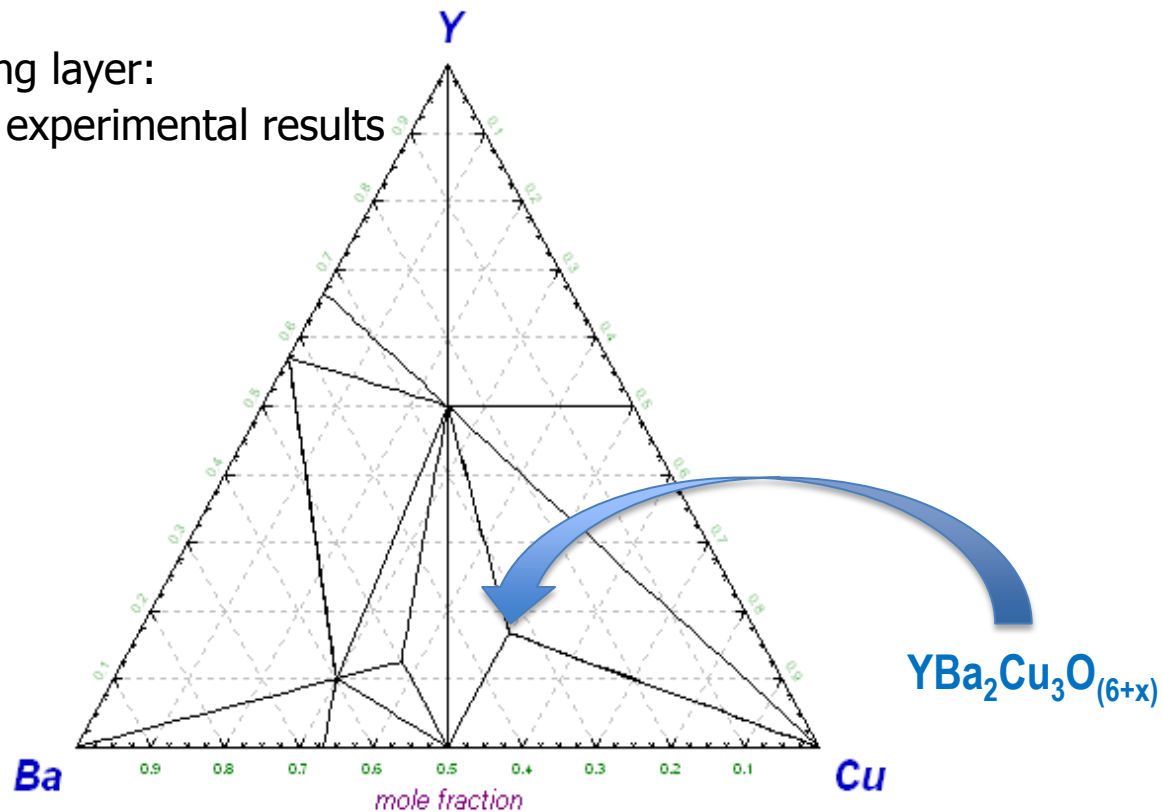
Project ELSA

■ Highlights

- Superconducting layer:
Explanation of experimental results

Y - Ba - Cu - O₂
1100 K, $p(\text{O}_2) = 0.21 \text{ atm}$

FactSage[®]



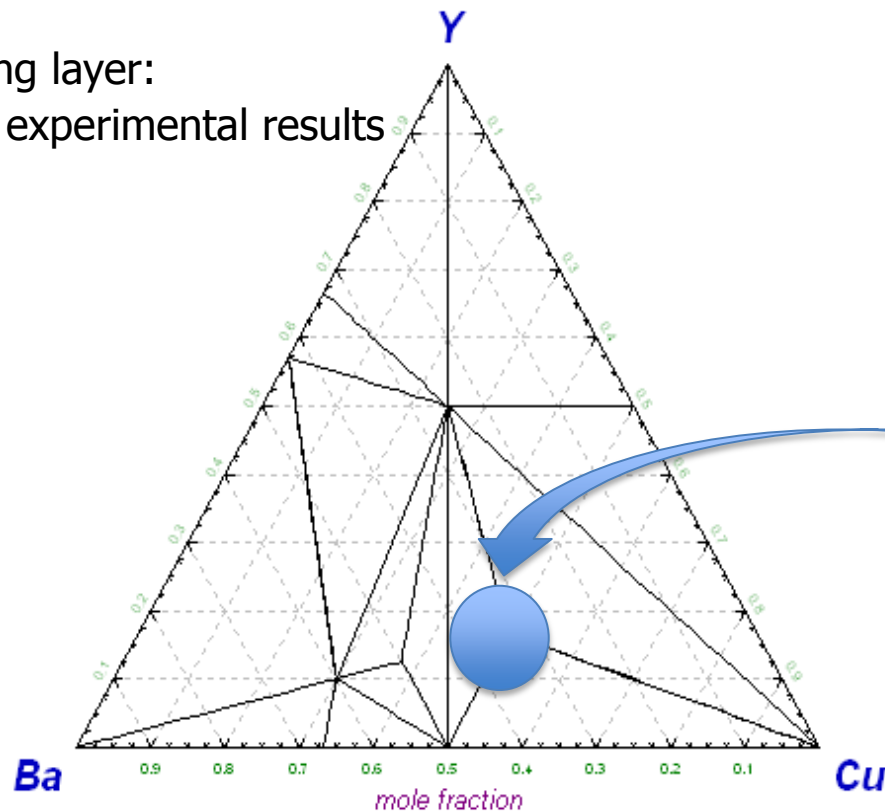
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- Superconducting layer:
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FactSage[®]



**Local stoichiometry
deviation before
crystallization**

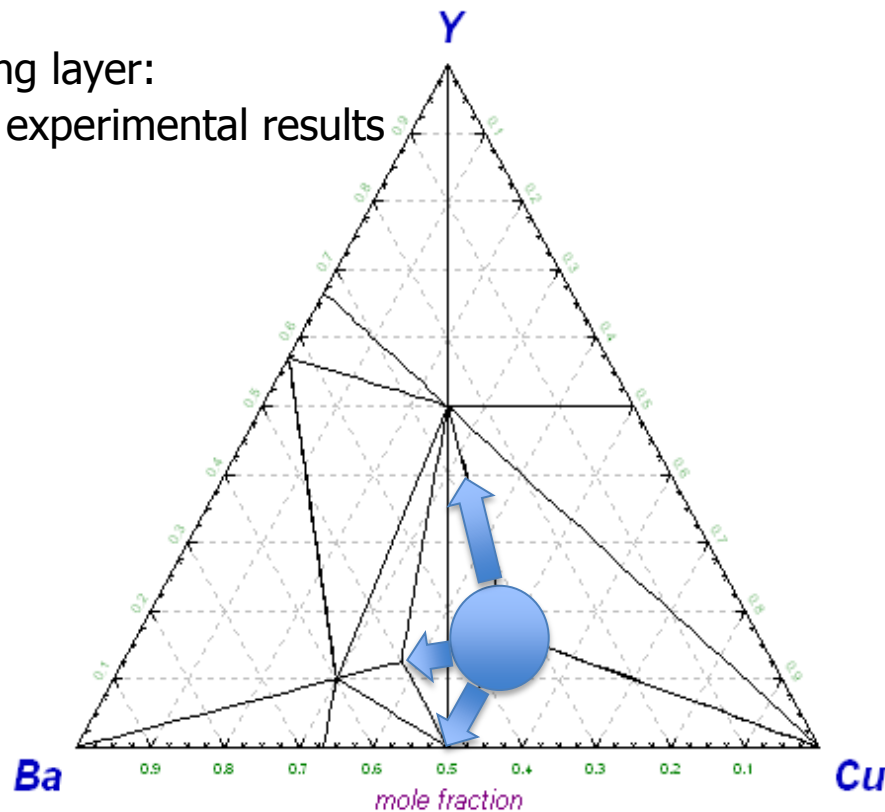
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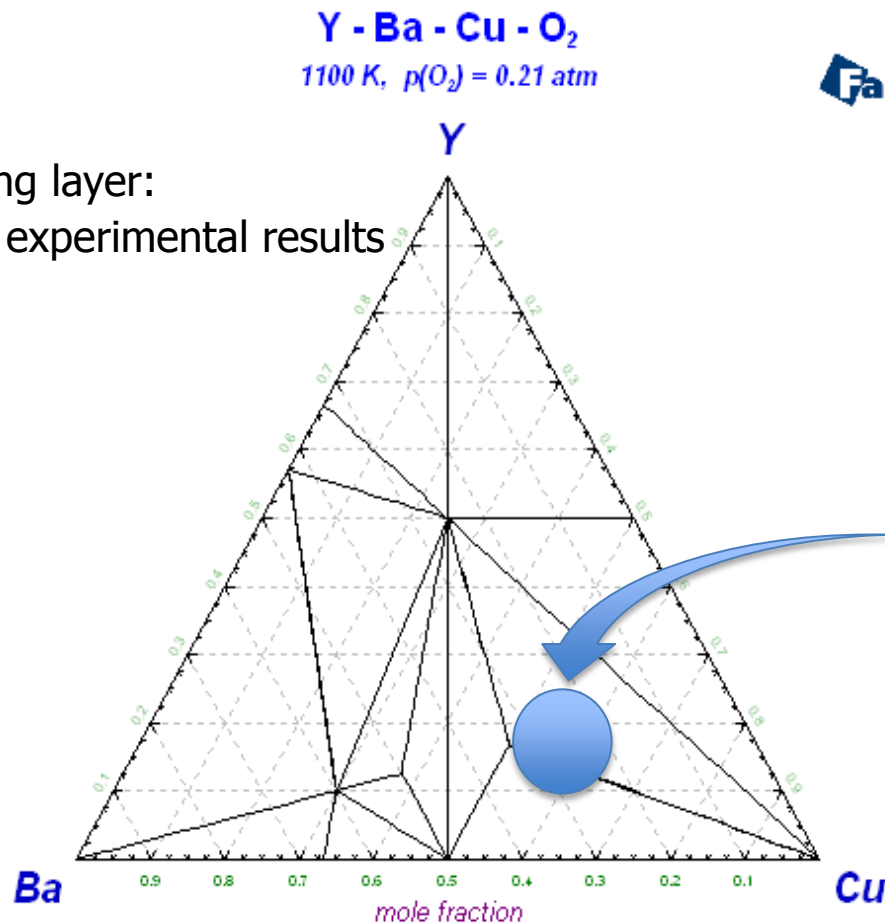


**Strong tendency to
secondary phases**

Project ELSA

■ Highlights

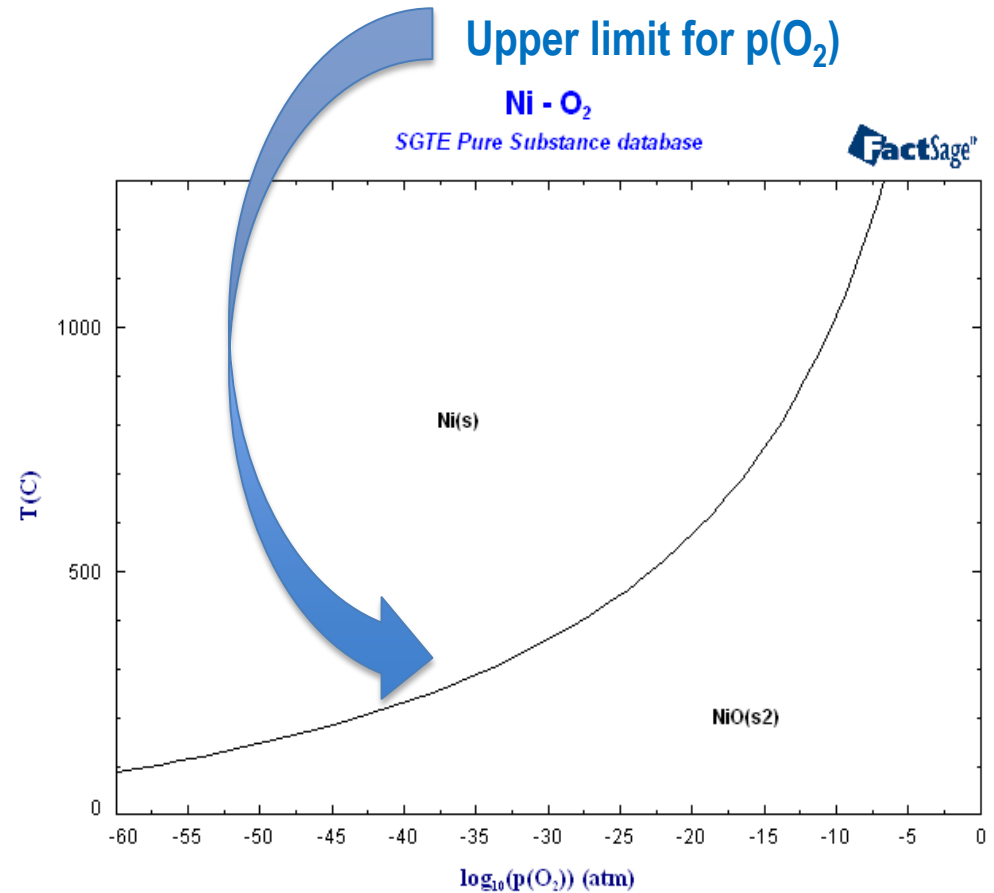
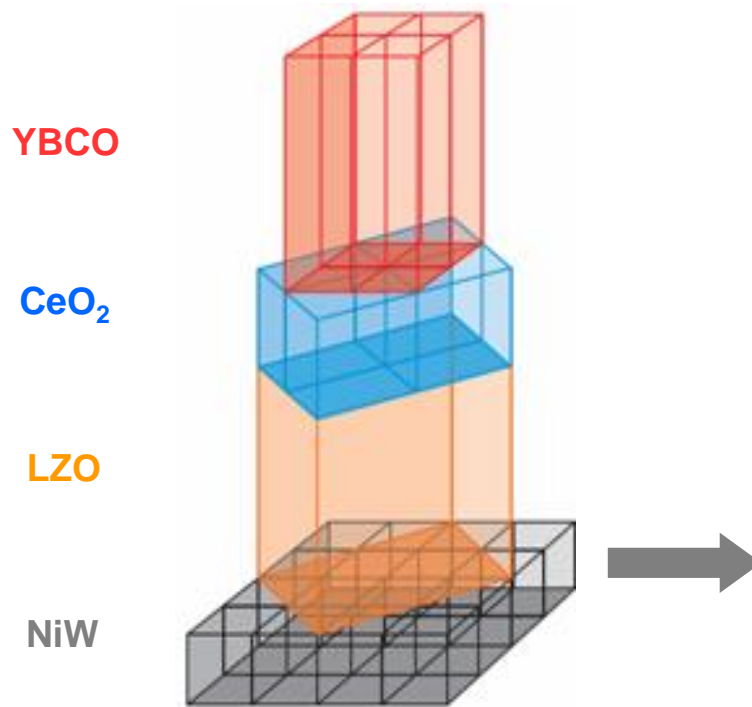
- Superconducting layer:
Explanation of experimental results



Higher phase purity by
 $\text{YBa}_{1.5}\text{Cu}_3\text{O}_{(6+x)}$
starting composition

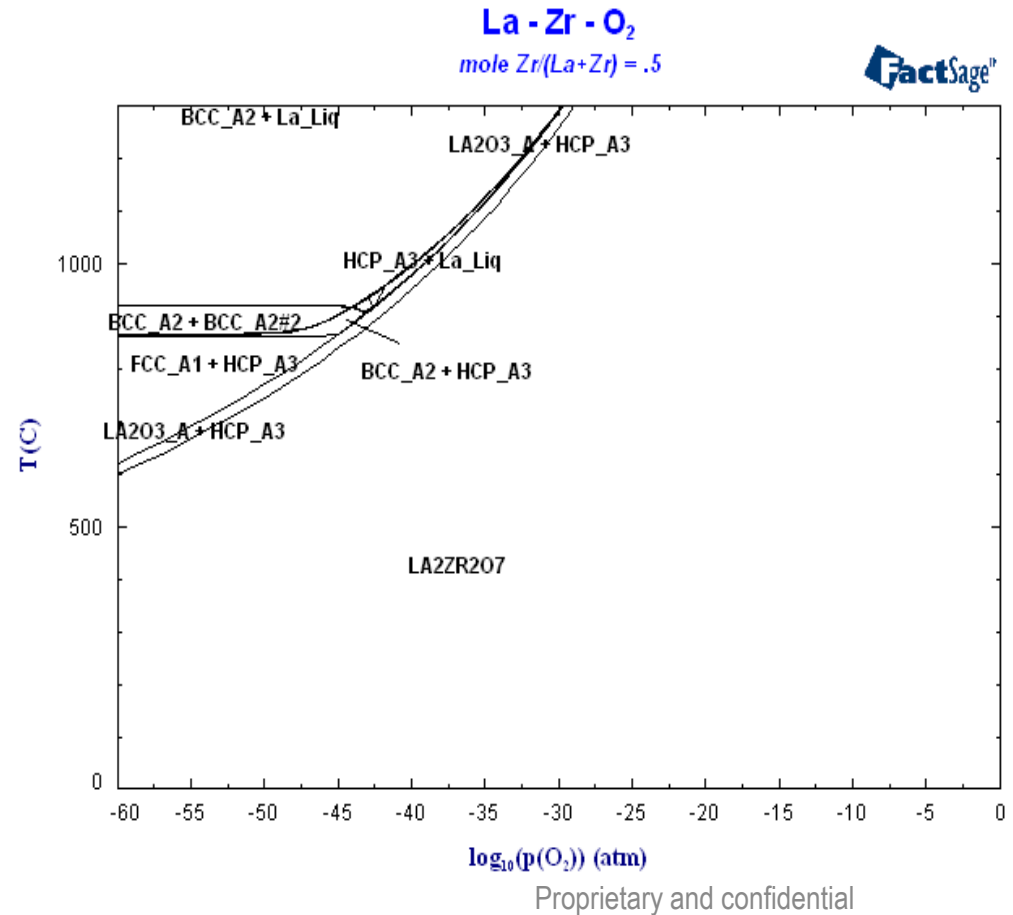
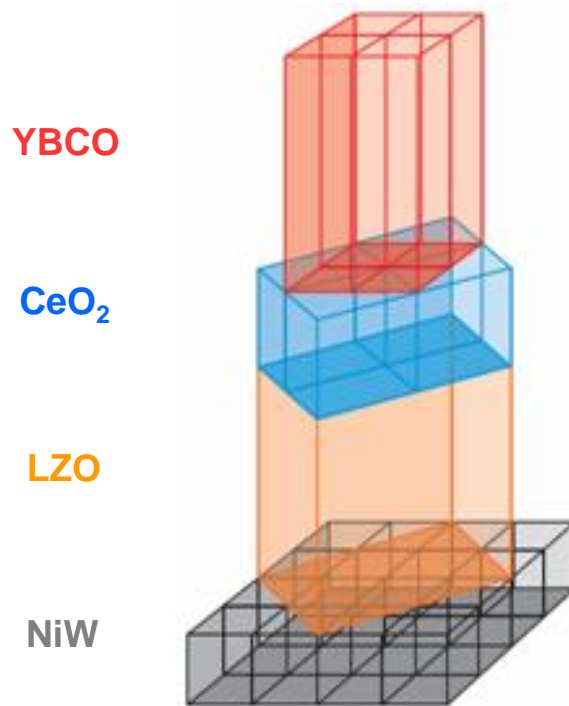
Project ELSA

- Highlights
 - layer interaction



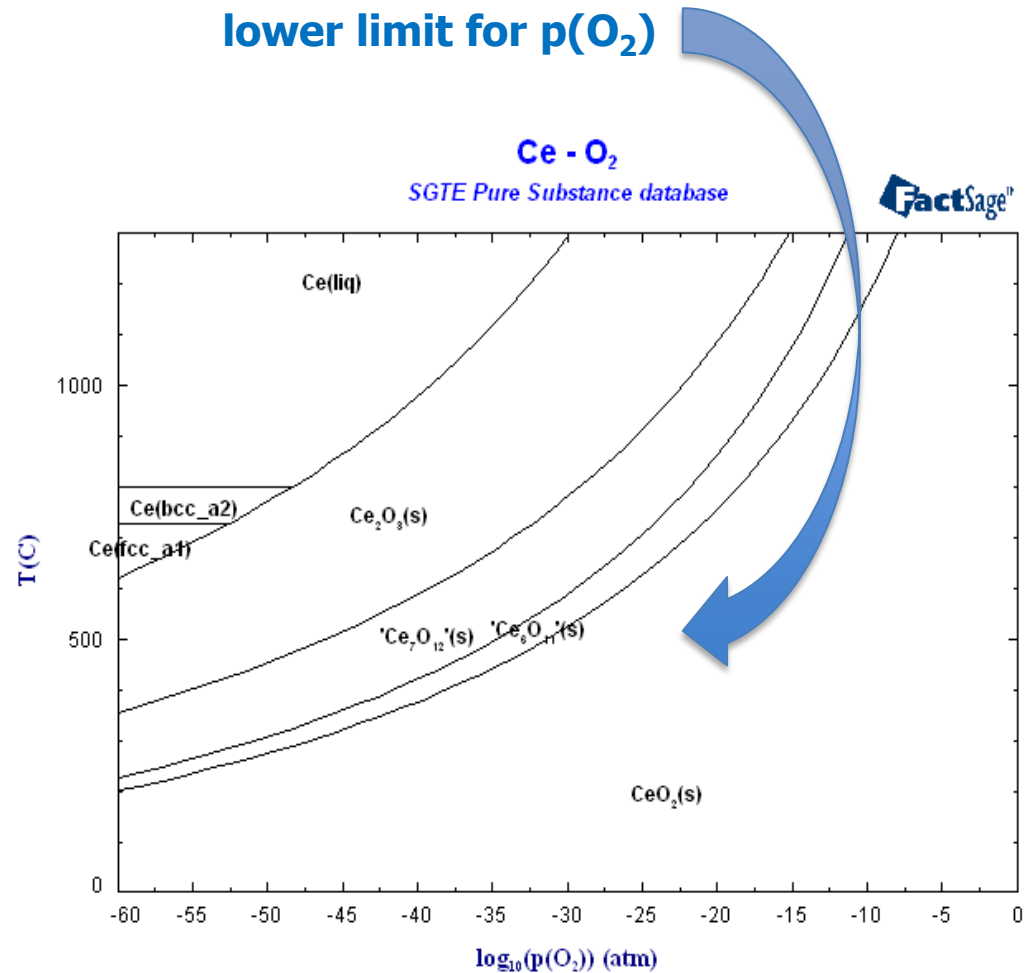
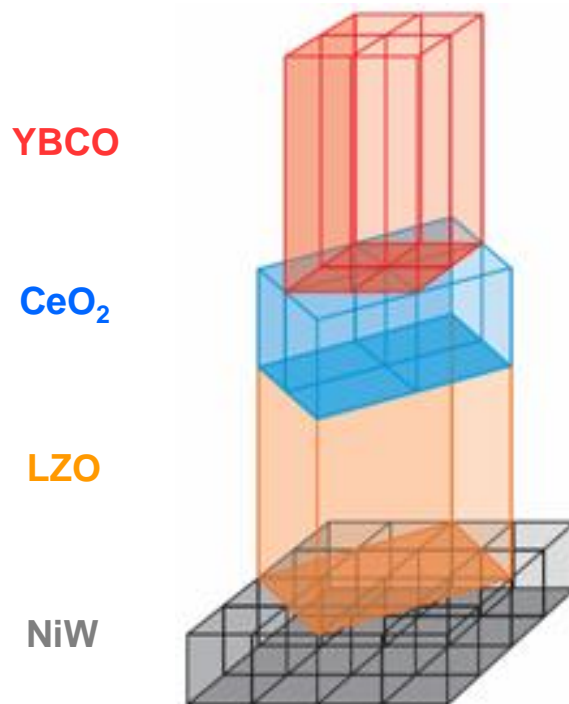
Project ELSA

- Highlights
 - layer interaction



Project ELSA

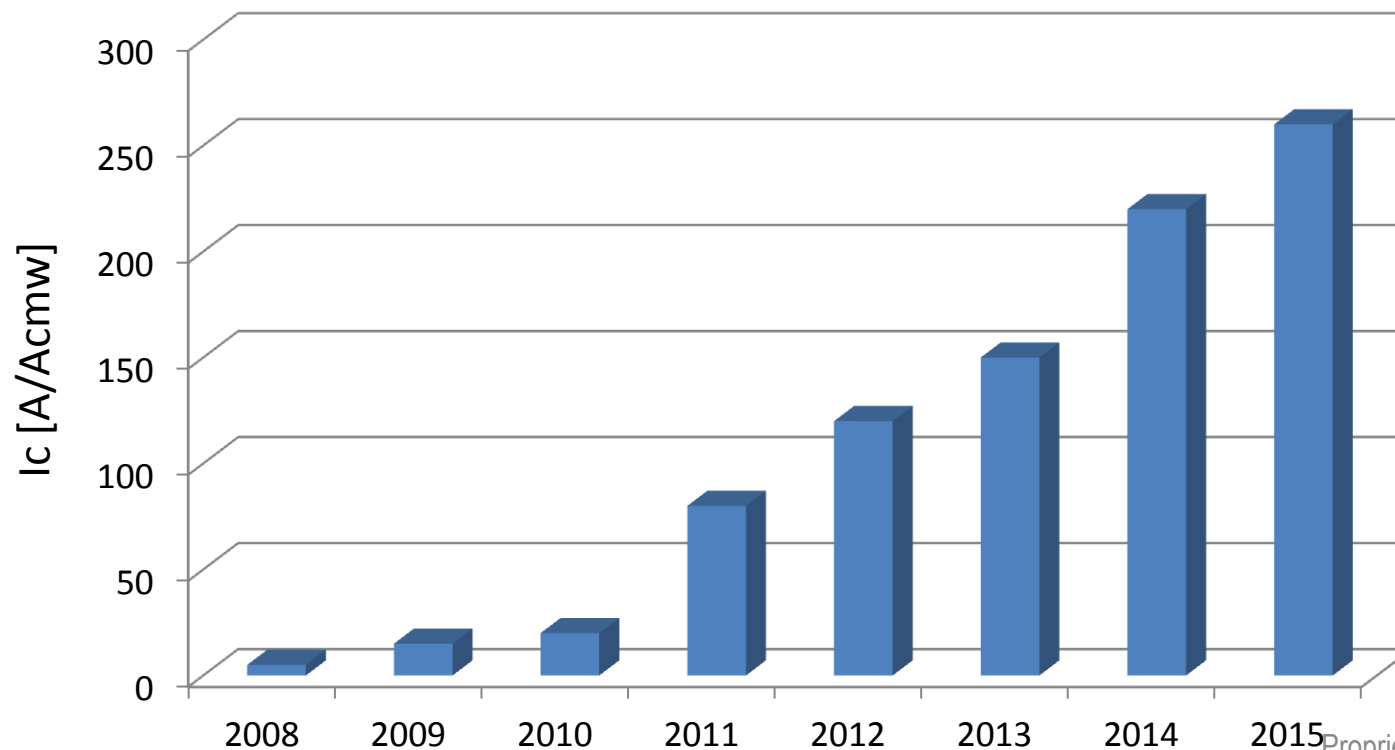
- Highlights
 - layer interaction



Performance

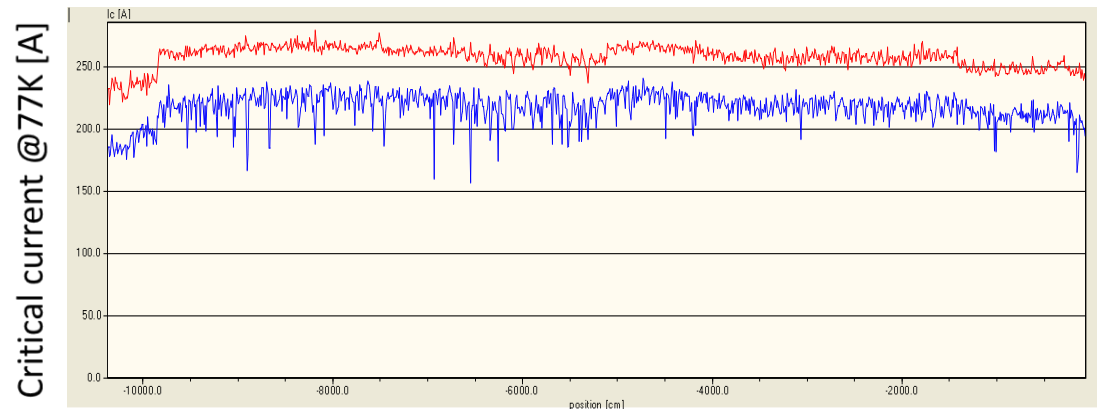
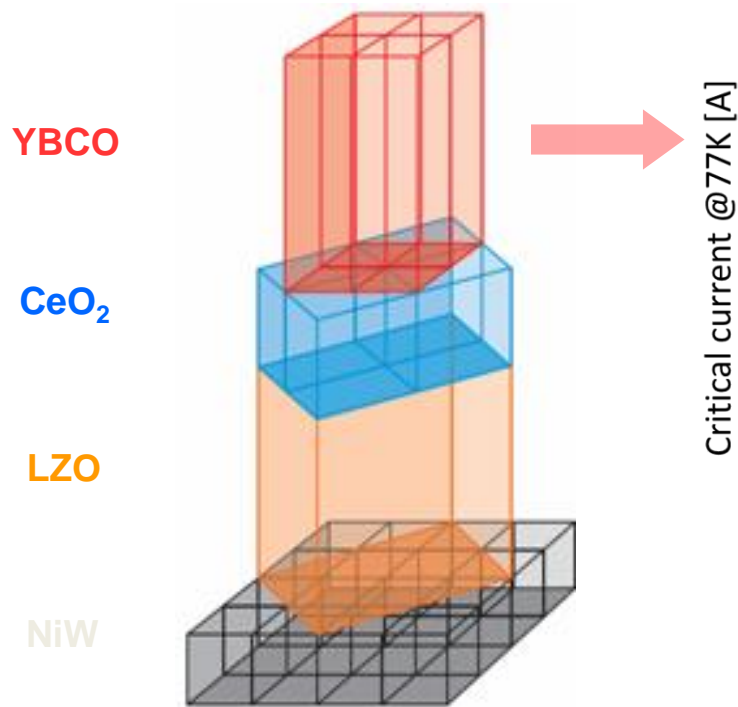
VDM Metals
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Length & Performance

- Superconducting properties of all-solution Coated Conductor



100 m tape length, 10 mm wide
Current density 1.7 MA/cm²

Summary

- HTS tapes
 - HTS materials enable new and highly efficient energy applications
 - Chemical solution deposition is considered to be the "most promising and most challenging process" for HTS tapes
 - **Computational thermochemistry** for interpretation of experimental results and for lowering experimental efforts in high temperature crystallization processes



Moving towards the future of
power engineering

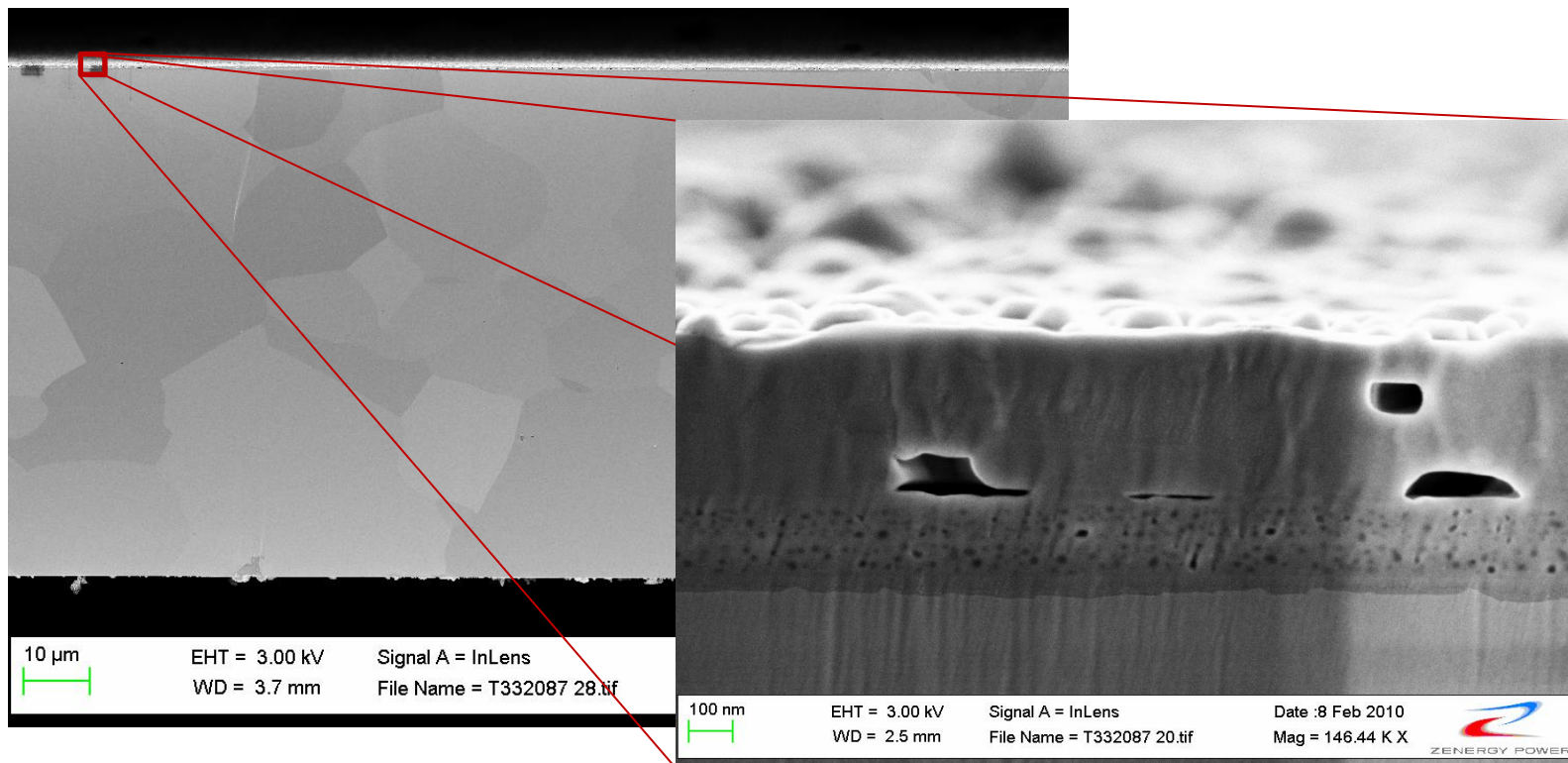
Thanks for your attention

Martina Falter

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Process Technology

- Ceramic multi-layer architecture on metallic substrate



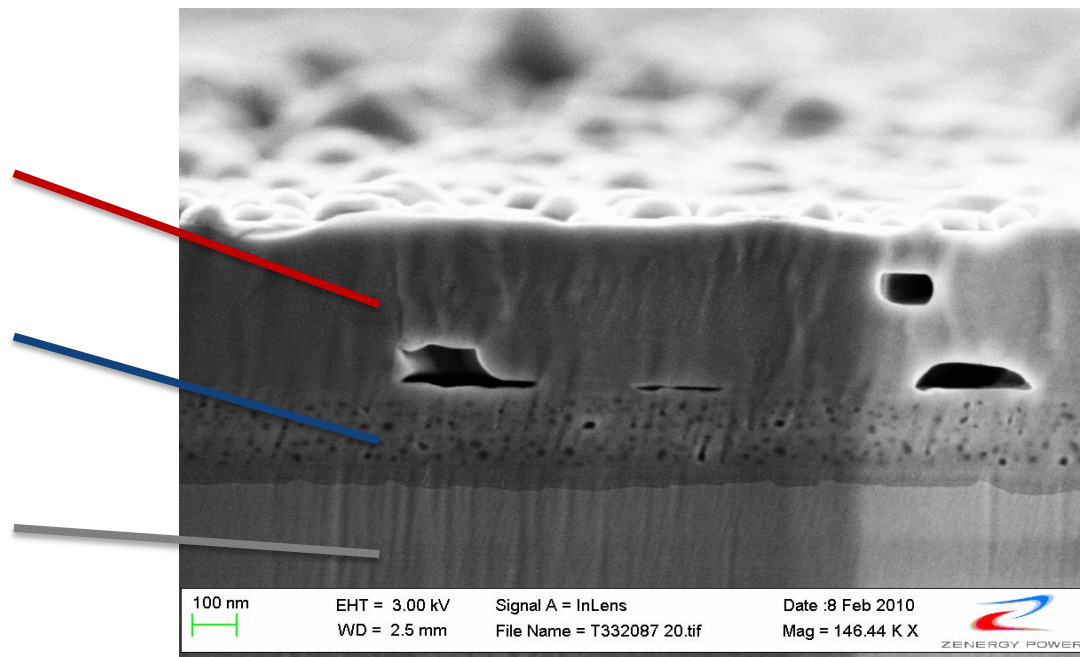
Process Technology

- Ceramic multi-layer architecture on metallic substrate

Superconducting layer
500-1000nm

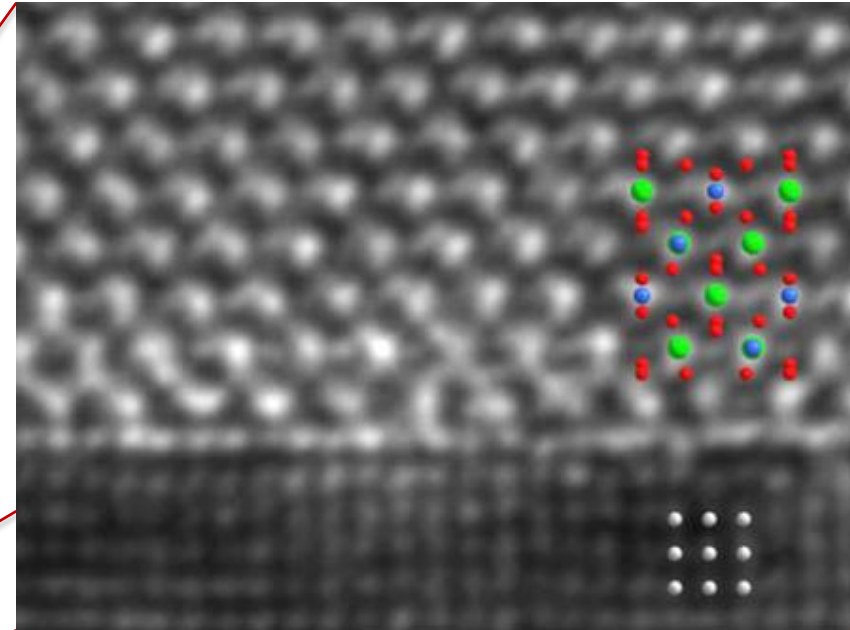
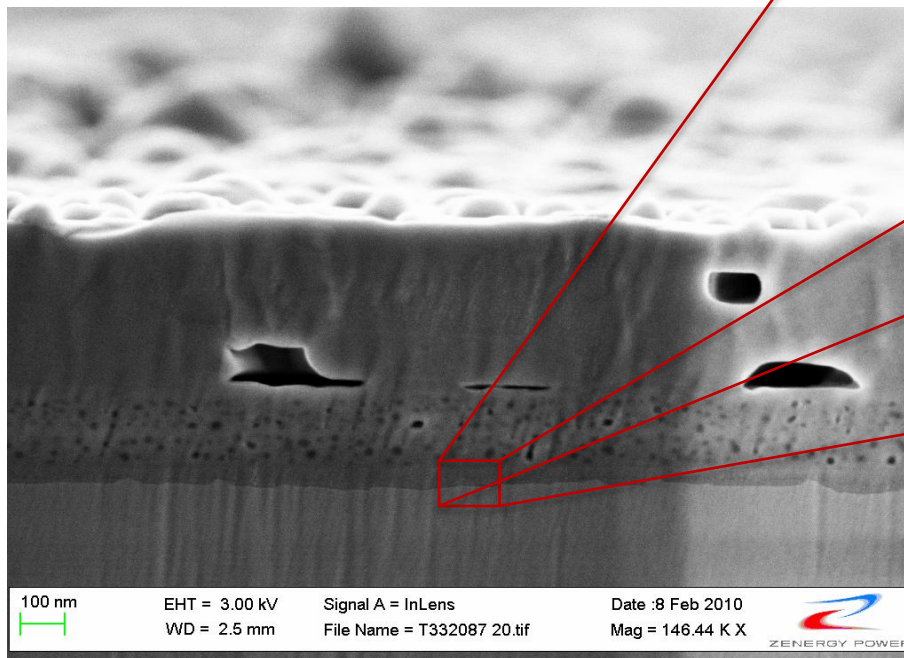
Buffer layer
250nm (3 layers)

Textured metallic substrate
60μm



Process Technology

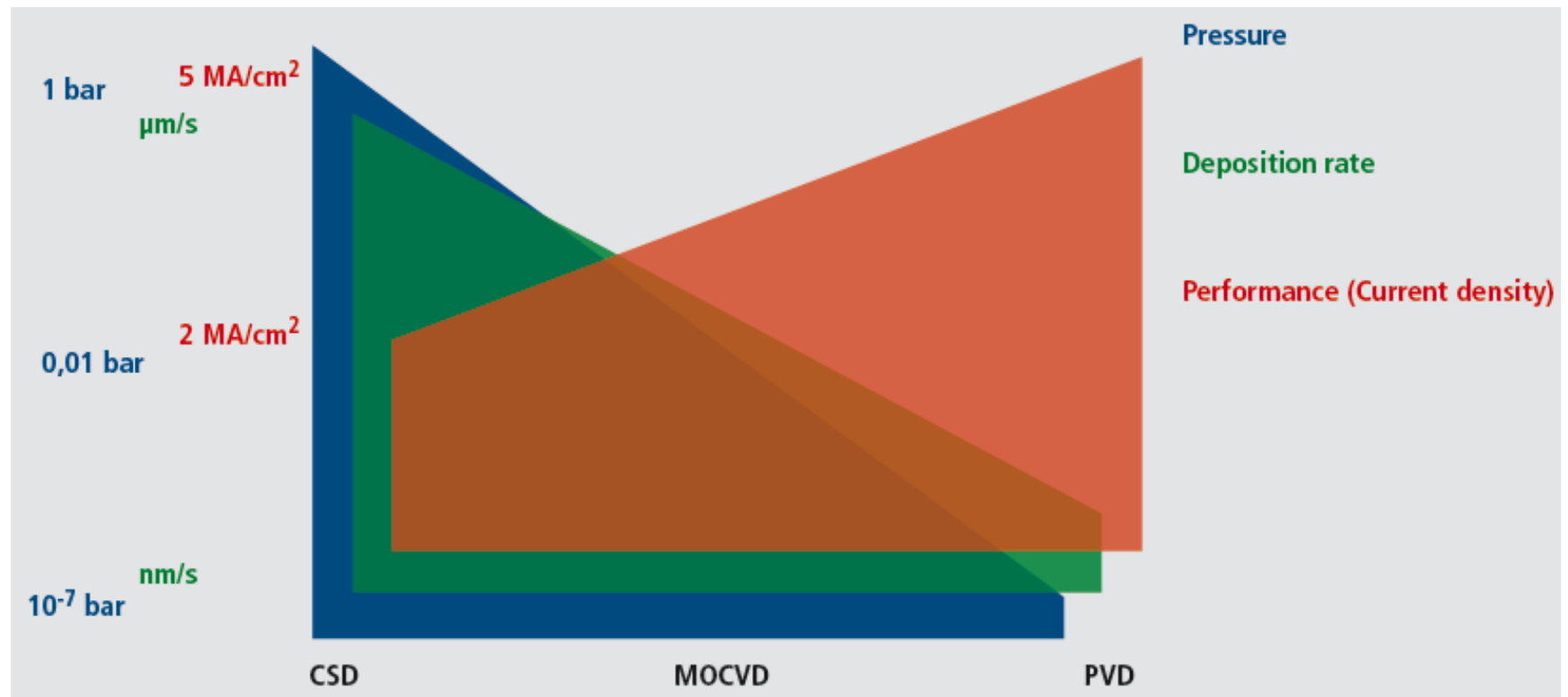
- Ceramic multi-layer architecture on metallic substrate



Epitaxial (orientated) growth of ceramic functional layers on textured metallic substrate

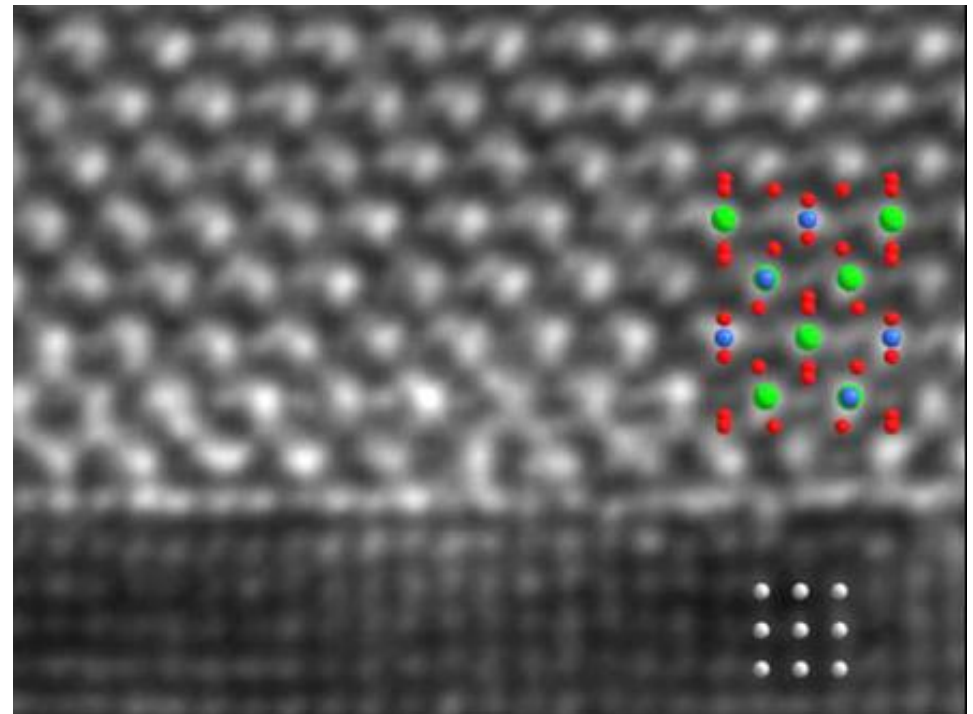
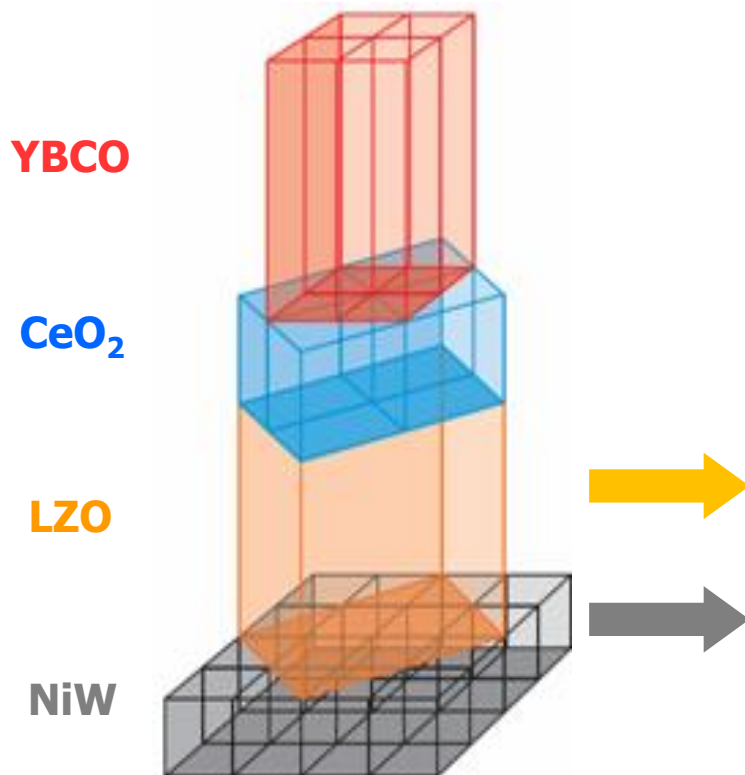
Process Technology

- Chemical solution deposition



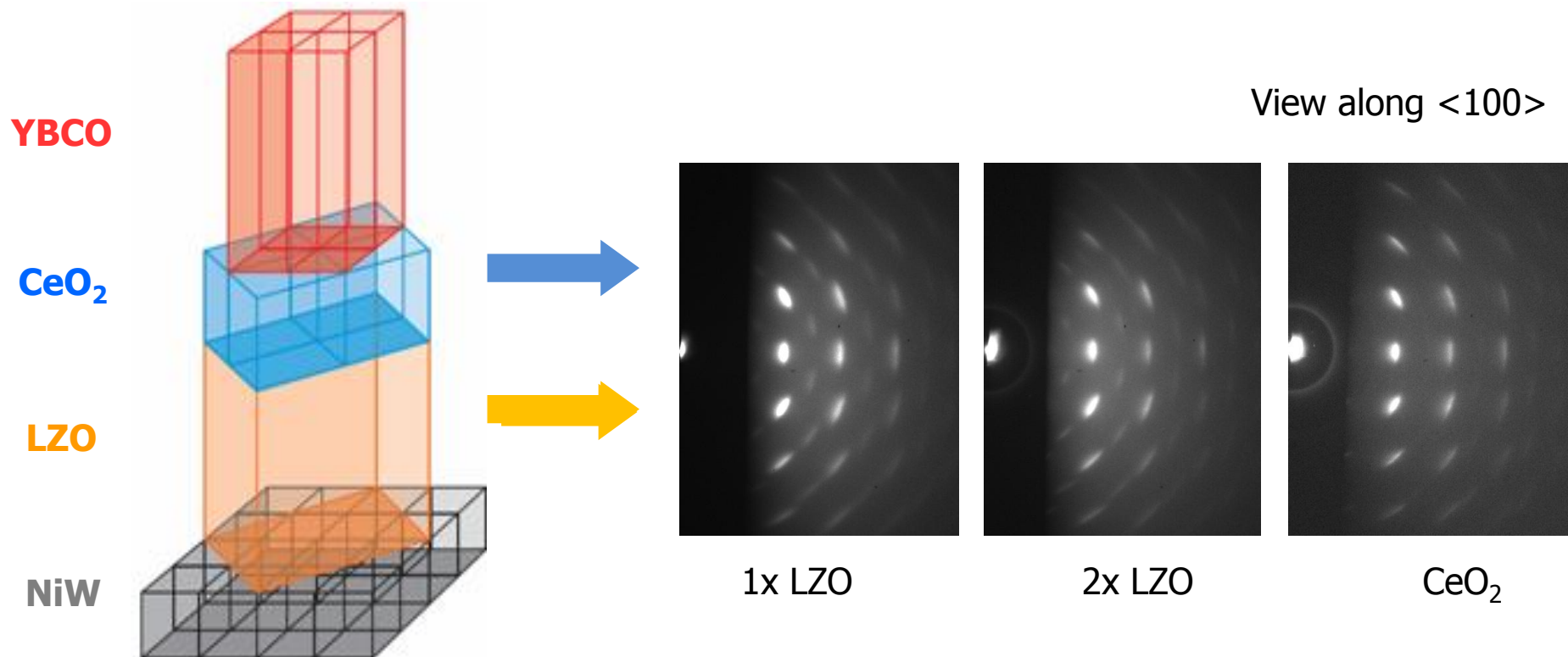
Process Technology

- Epitaxial growth

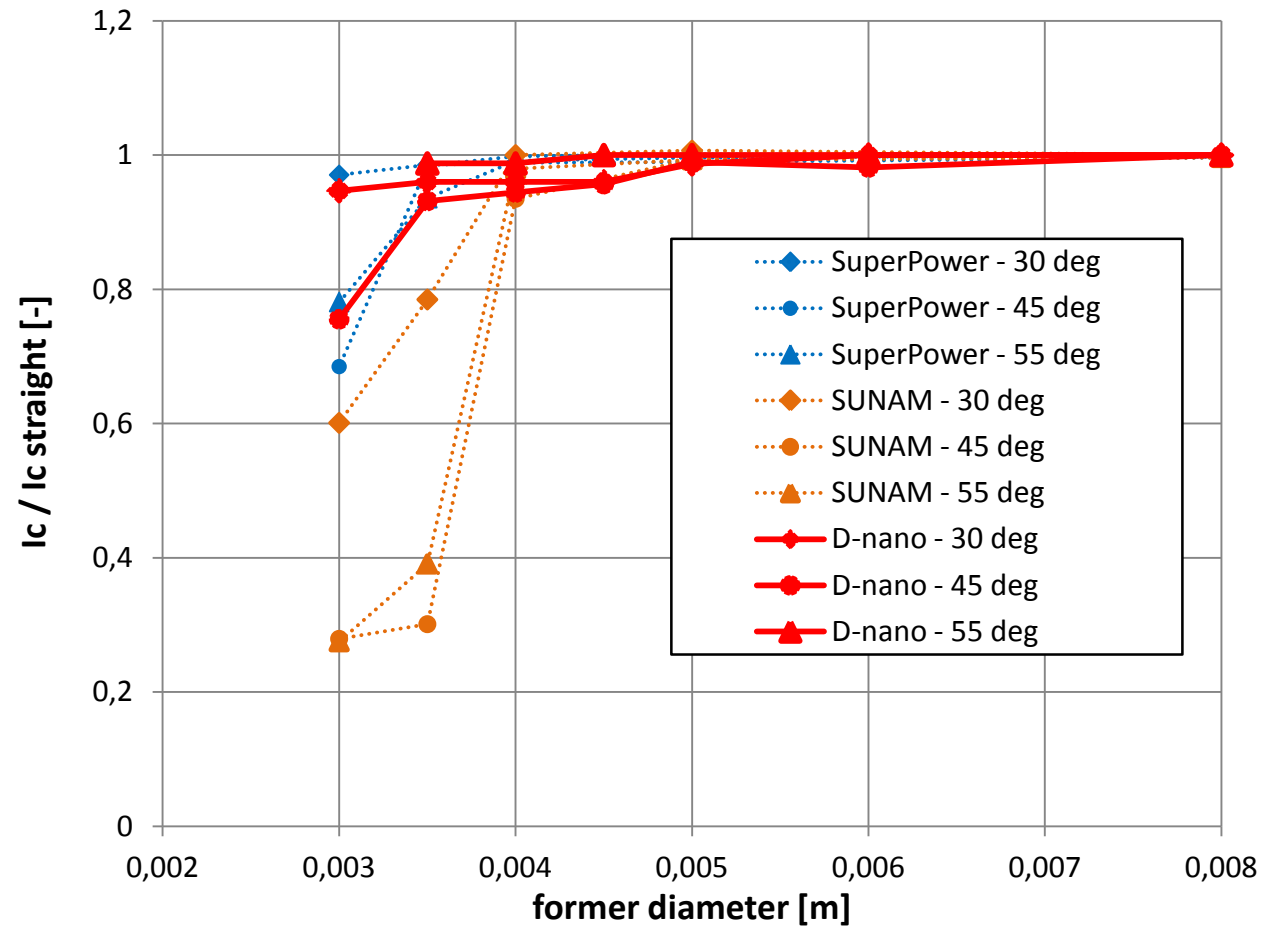
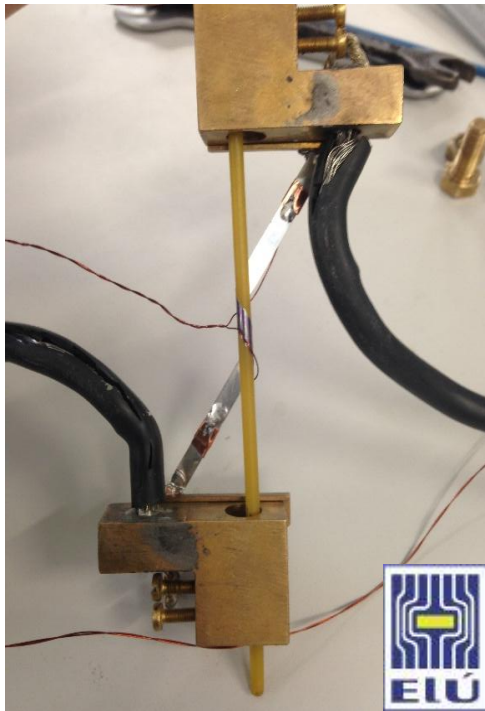


Process Technology

- Epitaxial growth



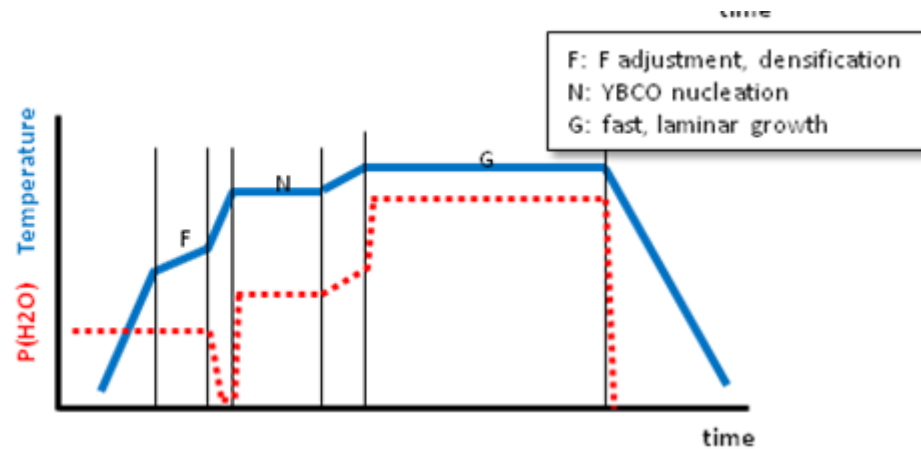
torsion-bending experiment



torsion-bending capability of D-Nano tape is comparable with SuperPower tape

Nucleation at the interface

ORNL (batch)



Grundidee ORNL – d-nano (kontinuierlich, 2 Stufen)

